Low-power dual unbuffered inverter Rev. 2 — 28 January 2019

### 1. General description

The 74AUP2GU04-Q100 provides two unbuffered inverting gates.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

### 2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)

  Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- ESD protection:
  - MIL-STD-883, method 3015 Class 3A. Exceeds 5000 V
  - HBM JESD22-A114F Class 3A. Exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V (C = 200 pF; R = 0  $\Omega$ )
- Low static power consumption;  $I_{CC} = 0.9 \ \mu A$  (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V

### 3. Ordering information

#### Table 1. Ordering information

Type number Package							
	Temperature range	Name	Description	Version			
74AUP2GU04GW-Q100	-40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363			
74AUP2GU04GM-Q100	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm	SOT886			

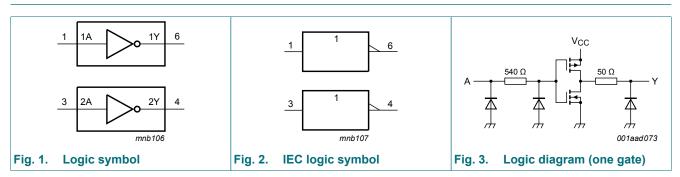
### 4. Marking

Table 2. Marking						
Type number	Marking code[1]					
74AUP2GU04GW-Q100	aD					
74AUP2GU04GM-Q100	aD					

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

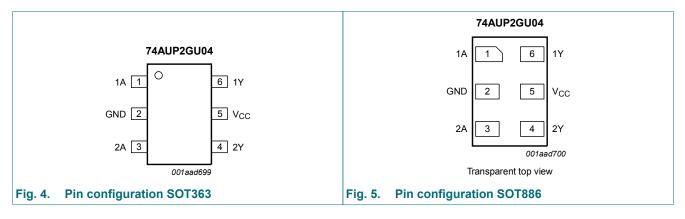
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### 5. Functional diagram



### 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description						
Symbol	Pin	Description				
1A	1	data input				
GND	2	ground (0 V)				
2A	3	data input				
2Y	4	data output				
V <sub>CC</sub>	5	supply voltage				
1Y	6	data output				

### 7. Functional description

### Table 4. Function table

*H* = *HIGH* voltage level; *L* = *LOW* voltage level.

Input	Output
nA	nY
L	Н
Н	L

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
Symbol	Falailletei	Conditions		IVIIII	IVIAX	Unit
V <sub>CC</sub>	supply voltage			-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Vo	output voltage		[2]	-0.5	V <sub>CC</sub> + 0.5	V
I <sub>O</sub>	output current	$V_{O}$ = 0 V to $V_{CC}$		-	±20	mA
I <sub>CC</sub>	supply current			-	50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb}$ = -40 °C to +125 °C	[3]	-	250	mW

[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

[2] The output voltage ratings may be exceeded if the output current ratings are observed.

[3] For SC-88 package: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K.

For XSON6 package: above 118 °C the value of Ptot derates linearly with 7.8 mW/K.

### 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage		0	$V_{CC}$	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	0	200	ns/V

# **10. Static characteristics**

### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	5 °C					
VIH	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	0.75 × V <sub>CC</sub>	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.25 × V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = GND \text{ or } V_{CC}$				
		$I_{\rm O}$ = -20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = GND \text{ or } V_{CC}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μA
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.5	μA
Cı	input capacitance	$V_{CC}$ = 0 V to 3.6 V; V <sub>1</sub> = GND or V <sub>CC</sub>	-	1.5	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.8	-	pF
T <sub>amb</sub> = -4	40 °C to +85 °C				1	-
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	0.75 × V <sub>CC</sub>	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.25 × V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = GND \text{ or } V_{CC}$				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		$I_0 = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V

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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = GND \text{ or } V_{CC}$				
V <sub>OL</sub> LC V <sub>OL</sub> LC I <sub>I</sub> in I <sub>CC</sub> su T <sub>amb</sub> = -40 ° V <sub>IH</sub> HI V <sub>IL</sub> LC V <sub>OH</sub> HI		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μA
T <sub>amb</sub> = -4	40 °C to +125 °C					
VIH	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	0.75 × V <sub>CC</sub>	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V			$0.25 \times V_{CC}$	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = GND or V <sub>CC</sub>				
		$I_{\rm O}$ = -20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = GND \text{ or } V_{CC}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
lı	input leakage current	$V_I$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA

# **11. Dynamic characteristics**

### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C		-40	°C to +12	25 °C	Unit
				Typ[1]	Мах	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 5 p	F								
t <sub>pd</sub>	propagation delay	nA to nY; see Fig. 6 [2]							
		V <sub>CC</sub> = 0.8 V	-	6.2	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	0.9	2.3	4.4	0.9	4.8	5.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	0.7	1.7	3.1	0.6	3.4	3.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	0.5	1.4	2.6	0.5	2.9	3.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.4	1.1	2.0	0.4	2.3	2.6	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.3	1.0	1.8	0.3	2.1	2.4	ns
C <sub>L</sub> = 10	pF	1				L	1		
t <sub>pd</sub>	propagation delay	nA to nY; see Fig. 6 [2]							
		V <sub>CC</sub> = 0.8 V	-	9.6	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.2	3.1	6.1	1.2	6.8	7.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.0	2.3	4.0	0.9	4.6	5.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	0.8	1.9	3.3	0.7	3.8	4.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.6	1.5	2.7	0.6	3.1	3.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.5	1.3	2.4	0.5	2.7	3.0	ns
C <sub>L</sub> = 15	pF	-	1			1	1		-
t <sub>pd</sub>	propagation delay	nA to nY; see Fig. 6 [2]							
		V <sub>CC</sub> = 0.8 V	-	13.0	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.6	3.8	7.9	1.4	8.8	9.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.3	2.8	4.9	1.1	5.7	6.3	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.0	2.3	4.0	0.9	4.7	5.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.8	1.9	3.2	0.8	3.7	4.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.7	1.6	2.9	0.7	3.3	3.7	ns
C <sub>L</sub> = 30	pF						•		
t <sub>pd</sub>	propagation delay	nA to nY; see Fig. 6 [2]							
		V <sub>CC</sub> = 0.8 V	-	23.2	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.4	6.0	13.1	2.2	14.8	16.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.0	4.2	7.6	1.8	9.0	9.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.7	3.6	6.1	1.5	7.2	8.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.9	4.8	1.3	5.7	6.3	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.2	2.5	4.3	1.1	5.1	5.7	ns

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Symbol	Parameter	Conditions	25 °C		-40 °C to +125 °C			Unit	
				Typ[1]	Мах	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 5 pl	F, 10 pF, 15 pF and	30 pF							
C <sub>PD</sub>	power dissipation	$f_i = 1 \text{ MHz}; V_1 = \text{GND to } V_{CC}$ [3][4]							
	capacitance	V <sub>CC</sub> = 0.8 V	-	1.1	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	1.1	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	1.3	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	1.5	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.0	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.5	-	-	-	-	pF

All typical values are measured at nominal  $V_{CC}$ . [1]

 $t_{\text{pd}}$  is the same as  $t_{\text{PLH}}$  and  $t_{\text{PHL}}.$ [2]

[3] All specified values are the average typical values over all stated loads.

 $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).  $P_D = C_{PD} x V_{CC}^2 x f_i x N + \Sigma (C_L x V_{CC}^2 x f_o)$  where: [4]

f<sub>i</sub> = input frequency in MHz;

fo = output frequency in MHz;

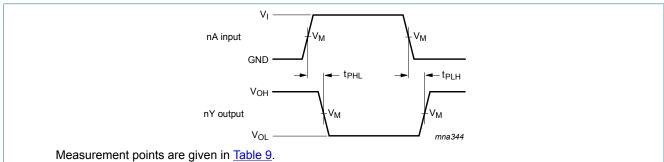
C<sub>L</sub> = load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$  = sum of the outputs.

### 11.1. Waveforms and test circuit



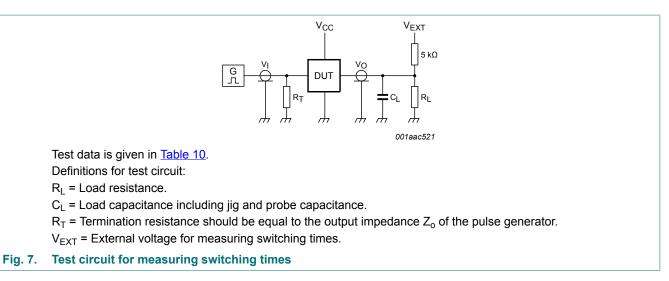
Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage drops that occur with the output load.

#### Fig. 6. The data input (nA) to output (nY) propagation delays

#### **Table 9. Measurement points**

Supply voltage	Output	Input				
V <sub>cc</sub>	V <sub>M</sub>	V <sub>M</sub>	VI	t <sub>r</sub> = t <sub>f</sub>		
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns		

#### Low-power dual unbuffered inverter



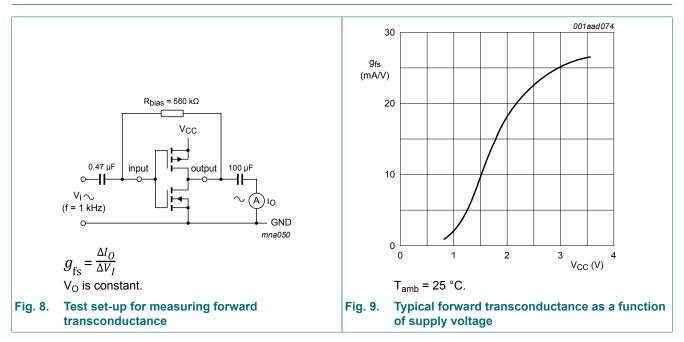
#### Table 10. Test data

Supply voltage	Load	V <sub>EXT</sub>				
V <sub>cc</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$	

[1] For measuring enable and disable times  $R_L = 5 k\Omega$ .

For measuring propagation delays, set-up and hold times and pulse width  $R_L$  = 1 M $\Omega$ .

### 12. Additional characteristics



# **13. Application information**

Some applications for the 74AUP2GU04-Q100 are:

- Linear amplifier (see Fig. 10)
- Crystal oscillator (see Fig. 11)

Remark: All values given are typical values unless otherwise specified.

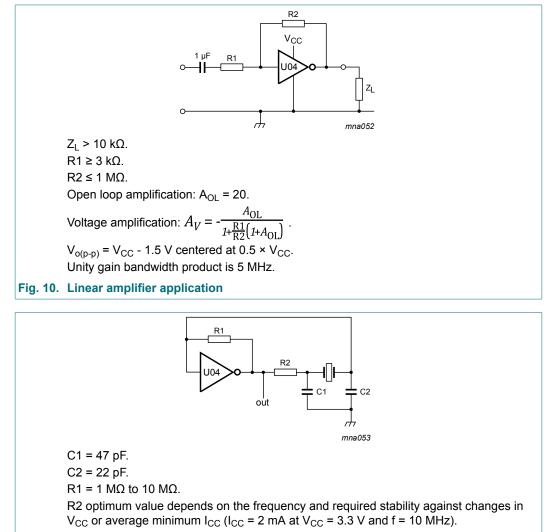
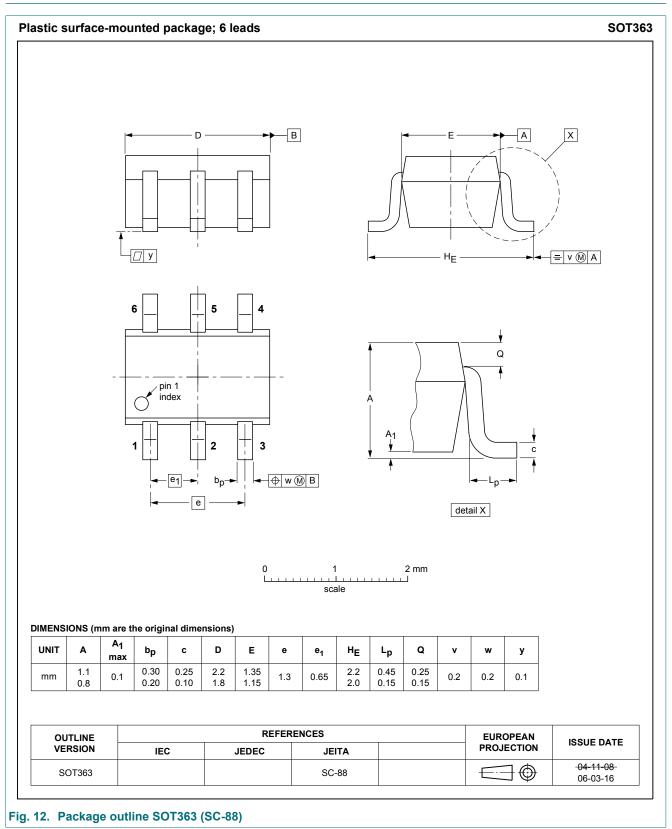
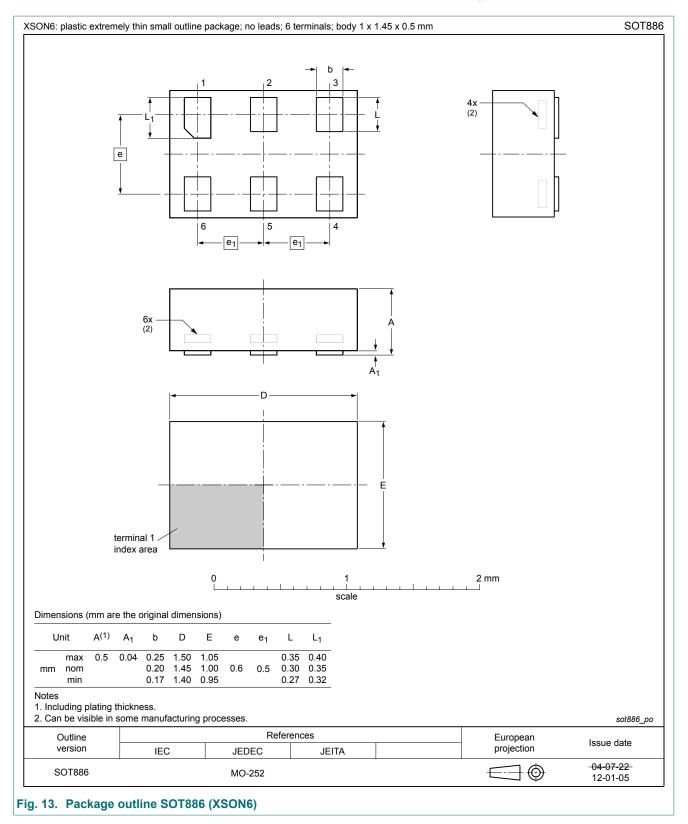


Fig. 11. Crystal oscillator application

# 14. Package outline



#### Low-power dual unbuffered inverter



# **15. Abbreviations**

Acronym	Description
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MIL	Military
MM	Machine Model

# 16. Revision history

Table 12. Revision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes			
74AUP2GU04_Q100 v.2	20190128	Product data sheet	-	74AUP2GU04_Q100 v.1			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Added type number 74AUP2GU04GM-Q100 (SOT886/XSON6).</li> </ul>						
74AUP2GU04_Q100 v.1	20141020	Product data sheet	-	-			

# 17. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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