

IGBT

High speed 5 FAST IGBT in TRENCHSTOP™ 5 technology copacked with RAPID 1 fast and soft anti parallel diode

IKP20N65F5

650V DuoPack IGBT and Diode
High speed switching series fifth generation

Data sheet

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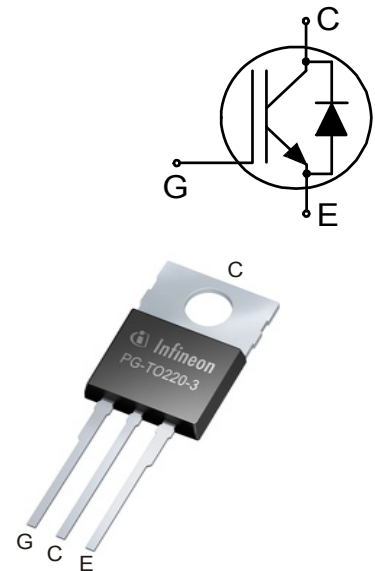
Features and Benefits:

High speed F5 technology offering

- Best-in-Class efficiency in hard switching and resonant topologies
- 650V breakdown voltage
- Low Q_g
- IGBT copacked with RAPID 1 fast and soft antiparallel diode
- Maximum junction temperature 175°C
- Qualified according to JEDEC for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models:
<http://www.infineon.com/igbt/>

Applications:

- Solar converters
- Uninterruptible power supplies
- Welding converters
- Mid to high range switching frequency converters



Key Performance and Package Parameters

Type	V_{CE}	I_C	$V_{CEsat}, T_{vj}=25^{\circ}C$	T_{vjmax}	Marking	Package
IKP20N65F5	650V	20A	1.6V	175°C	K20EF5	PG-TO220-3

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Maximum Ratings

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^{\circ}\text{C}$	V_{CE}	650	V
DC collector current, limited by T_{vjmax} $T_C = 25^{\circ}\text{C}$ $T_C = 100^{\circ}\text{C}$	I_C	42.0 21.0	A
Pulsed collector current, t_p limited by T_{vjmax}	I_{Cpuls}	60.0	A
Turn off safe operating area $V_{CE} \leq 650\text{V}$, $T_{vj} \leq 175^{\circ}\text{C}$, $t_p = 1\mu\text{s}$	-	60.0	A
Diode forward current, limited by T_{vjmax} $T_C = 25^{\circ}\text{C}$ $T_C = 100^{\circ}\text{C}$	I_F	20.0 10.0	A
Diode pulsed current, t_p limited by T_{vjmax}	I_{Fpuls}	60.0	A
Gate-emitter voltage Transient Gate-emitter voltage ($t_p \leq 10\mu\text{s}$, $D < 0.010$)	V_{GE}	± 20 ± 30	V
Power dissipation $T_C = 25^{\circ}\text{C}$ Power dissipation $T_C = 100^{\circ}\text{C}$	P_{tot}	125.0 63.0	W
Operating junction temperature	T_{vj}	$-40 \dots +175$	$^{\circ}\text{C}$
Storage temperature	T_{stg}	$-55 \dots +150$	$^{\circ}\text{C}$
Soldering temperature, wave soldering 1.6 mm (0.063 in.) from case for 10s		260	$^{\circ}\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		1.20	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		2.20	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		62	K/W

Electrical Characteristic, at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0V, I_C = 0.20mA$	650	-	-	V
Collector-emitter saturation voltage	V_{CEsat}	$V_{GE} = 15.0V, I_C = 20.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 125^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.60 1.80 1.90	2.10 - -	V
Diode forward voltage	V_F	$V_{GE} = 0V, I_F = 10.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 125^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.45 1.40 1.40	1.80 - -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.20mA, V_{CE} = V_{GE}$	3.2	4.0	4.8	V
Zero gate voltage collector current	I_{CES}	$V_{CE} = 650V, V_{GE} = 0V$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	- -	- -	40.0 4000.0	μA
Gate-emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = 20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE} = 20V, I_C = 20.0A$	-	24.0	-	S

Electrical Characteristic, at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C_{ies}	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	-	1200	-	pF
Output capacitance	C_{oes}		-	30	-	
Reverse transfer capacitance	C_{res}		-	5	-	
Gate charge	Q_G	$V_{CC} = 520V, I_C = 20.0A, V_{GE} = 15V$	-	48.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7.0	-	nH

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at $T_{vj} = 25^{\circ}\text{C}$						
Turn-on delay time	$t_{d(\text{on})}$	$T_{vj} = 25^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 10.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $r_G = 32.0\Omega$, $L\sigma = 30\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	20	-	ns
Rise time	t_r		-	11	-	ns
Turn-off delay time	$t_{d(\text{off})}$		-	165	-	ns
Fall time	t_f		-	17	-	ns
Turn-on energy	E_{on}		-	0.16	-	mJ
Turn-off energy	E_{off}		-	0.06	-	mJ
Total switching energy	E_{ts}		-	0.22	-	mJ

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 2.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $r_G = 32.0\Omega$, $L\sigma = 30\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	18	-	ns
Rise time	t_r		-	3	-	ns
Turn-off delay time	$t_{d(off)}$		-	170	-	ns
Fall time	t_f		-	30	-	ns
Turn-on energy	E_{on}		-	0.04	-	mJ
Turn-off energy	E_{off}		-	0.02	-	mJ
Total switching energy	E_{ts}		-	0.06	-	mJ

Diode Characteristic, at $T_{vj} = 25^{\circ}\text{C}$

Diode reverse recovery time	t_{rr}	$T_{vj} = 25^{\circ}\text{C}$, $V_R = 400\text{V}$, $I_F = 10.0\text{A}$, $di_F/dt = 1300\text{A}/\mu\text{s}$	-	53	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.28	-	μC
Diode peak reverse recovery current	I_{rrm}		-	10.5	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-235	-	$\text{A}/\mu\text{s}$
Diode reverse recovery time	t_{rr}	$T_{vj} = 25^{\circ}\text{C}$, $V_R = 400\text{V}$, $I_F = 2.0\text{A}$, $di_F/dt = 1000\text{A}/\mu\text{s}$	-	25	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.12	-	μC
Diode peak reverse recovery current	I_{rrm}		-	8.1	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-630	-	$\text{A}/\mu\text{s}$

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

IGBT Characteristic, at $T_{vj} = 150^{\circ}\text{C}$

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 150^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 10.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $r_G = 32.0\Omega$, $L\sigma = 30\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	19	-	ns
Rise time	t_r		-	13	-	ns
Turn-off delay time	$t_{d(off)}$		-	200	-	ns
Fall time	t_f		-	11	-	ns
Turn-on energy	E_{on}		-	0.26	-	mJ
Turn-off energy	E_{off}		-	0.11	-	mJ
Total switching energy	E_{ts}		-	0.37	-	mJ
Turn-on delay time	$t_{d(on)}$	$T_{vj} = 150^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 2.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $r_G = 32.0\Omega$, $L\sigma = 30\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	16	-	ns
Rise time	t_r		-	4	-	ns
Turn-off delay time	$t_{d(off)}$		-	230	-	ns
Fall time	t_f		-	43	-	ns
Turn-on energy	E_{on}		-	0.07	-	mJ
Turn-off energy	E_{off}		-	0.02	-	mJ
Total switching energy	E_{ts}		-	0.09	-	mJ

Diode Characteristic, at $T_{vj} = 150^{\circ}\text{C}$

Diode reverse recovery time	t_{rr}	$T_{vj} = 150^{\circ}\text{C},$ $V_R = 400\text{V},$ $I_F = 10.0\text{A},$ $di_F/dt = 1000\text{A}/\mu\text{s}$	-	87	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.63	-	μC
Diode peak reverse recovery current	I_{rrm}		-	13.1	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-186	-	$\text{A}/\mu\text{s}$
Diode reverse recovery time	t_{rr}	$T_{vj} = 150^{\circ}\text{C},$ $V_R = 400\text{V},$ $I_F = 2.0\text{A},$ $di_F/dt = 800\text{A}/\mu\text{s}$	-	45	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.29	-	μC
Diode peak reverse recovery current	I_{rrm}		-	11.3	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-440	-	$\text{A}/\mu\text{s}$

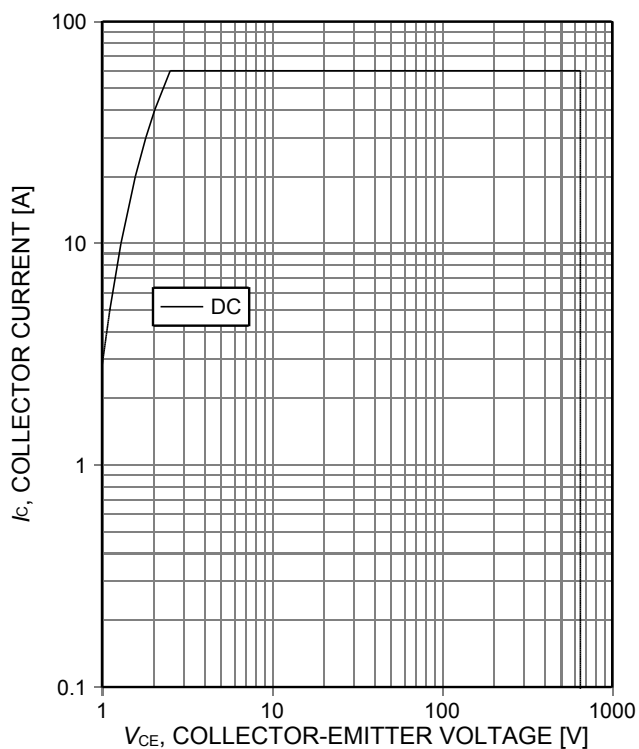


Figure 1. **Forward bias safe operating area**
($D=0$, $T_C=25^\circ\text{C}$, $T_{vj}\leq 175^\circ\text{C}$; $V_{GE}=15\text{V}$.
Recommended use at $V_{GE}\geq 7.5\text{V}$)

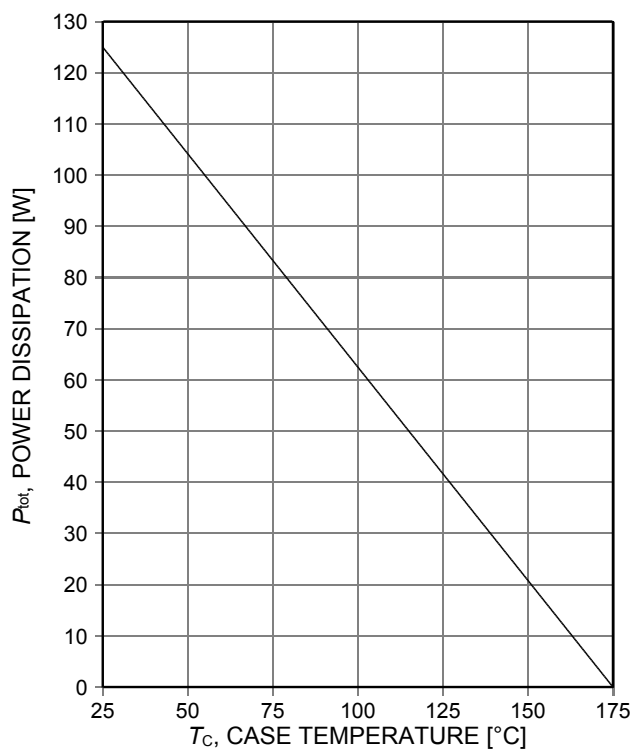


Figure 2. **Power dissipation as a function of case temperature**
($T_{vj}\leq 175^\circ\text{C}$)

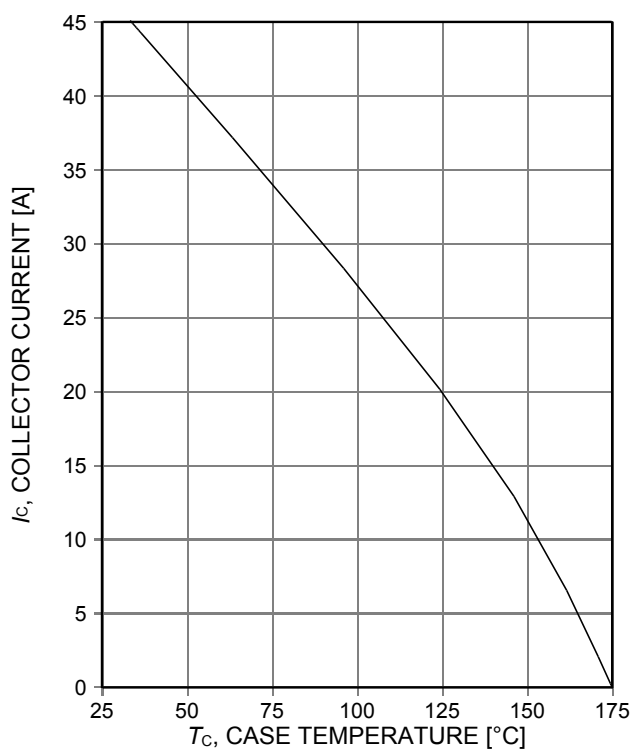


Figure 3. **Collector current as a function of case temperature**
($V_{GE}\geq 15\text{V}$, $T_{vj}\leq 175^\circ\text{C}$)

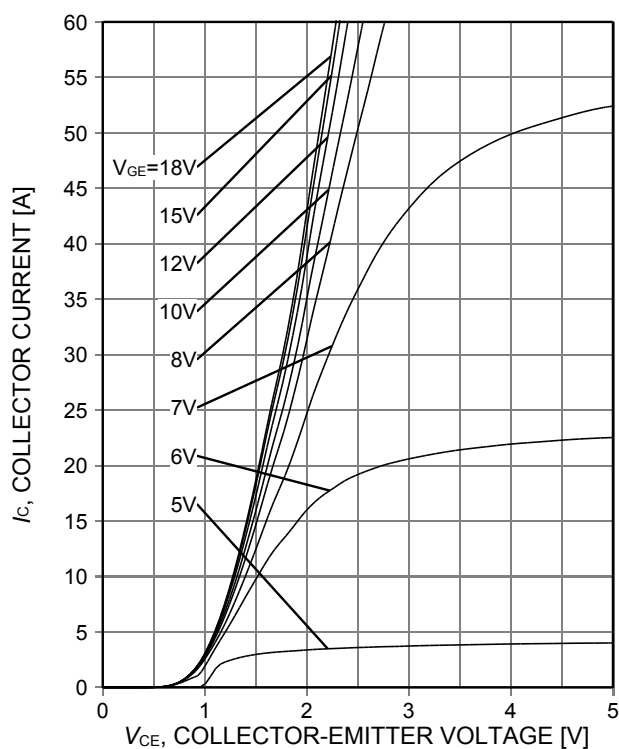


Figure 4. **Typical output characteristic**
($T_{vj}=25^\circ\text{C}$)

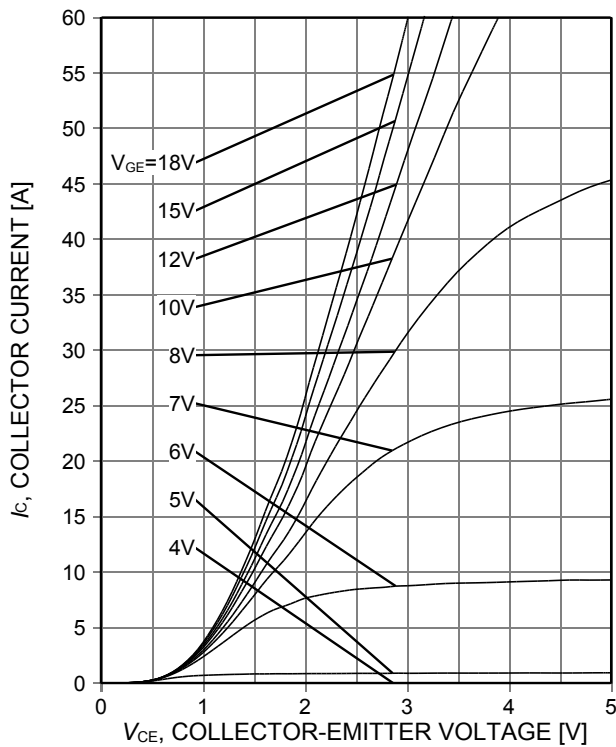


Figure 5. **Typical output characteristic**
($T_{vj}=150^{\circ}\text{C}$)

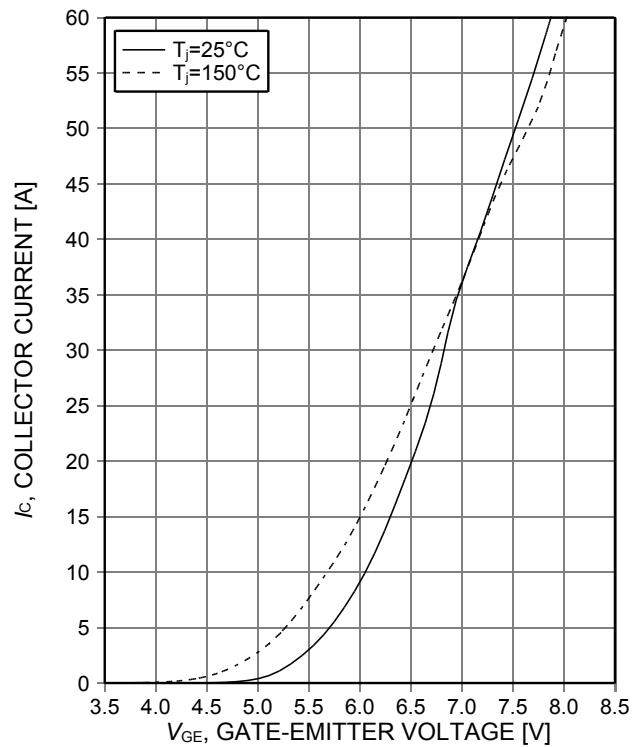


Figure 6. **Typical transfer characteristic**
($V_{ce}=20\text{V}$)

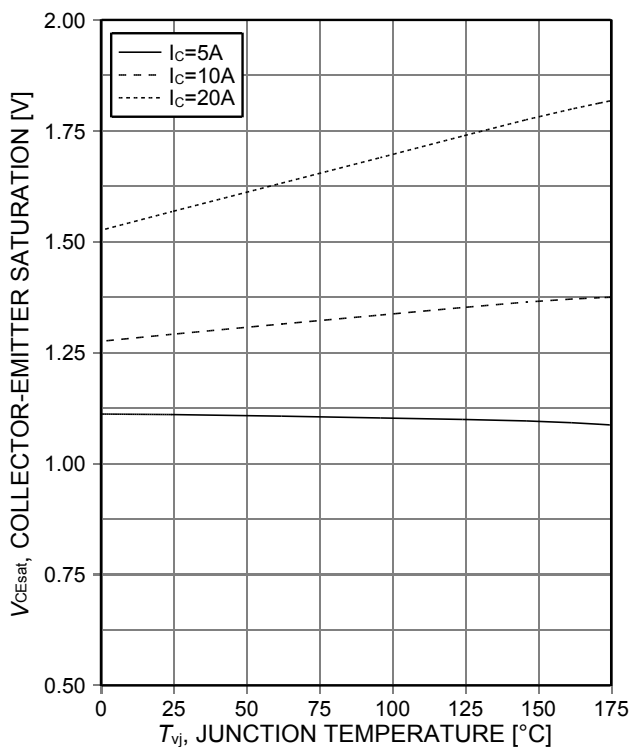


Figure 7. **Typical collector-emitter saturation voltage as a function of junction temperature**
($V_{ge}=15\text{V}$)

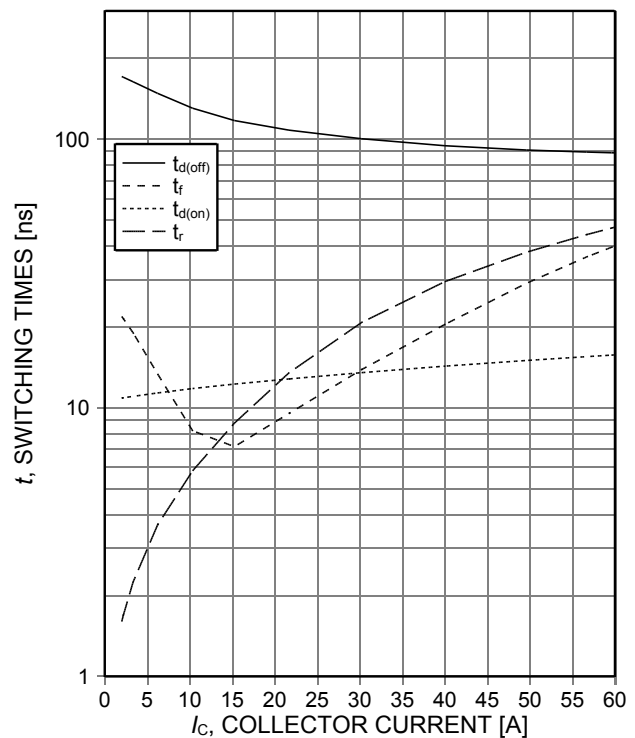


Figure 8. **Typical switching times as a function of collector current**
(inductive load, $T_{vj}=150^{\circ}\text{C}$, $V_{ce}=400\text{V}$, $V_{ge}=15/0\text{V}$, $r_g=32\Omega$, Dynamic test circuit in Figure E)

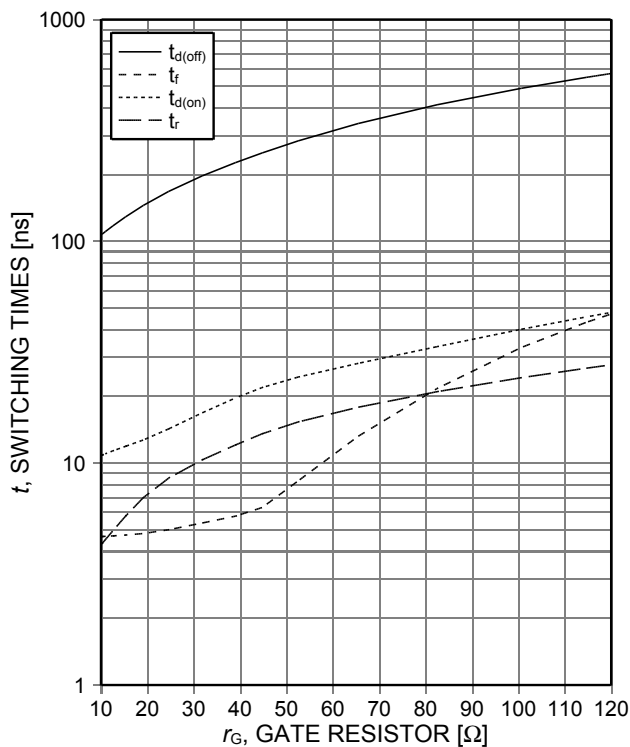


Figure 9. **Typical switching times as a function of gate resistor**
(inductive load, $T_{vj}=150^{\circ}\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_C=10\text{A}$, Dynamic test circuit in Figure E)

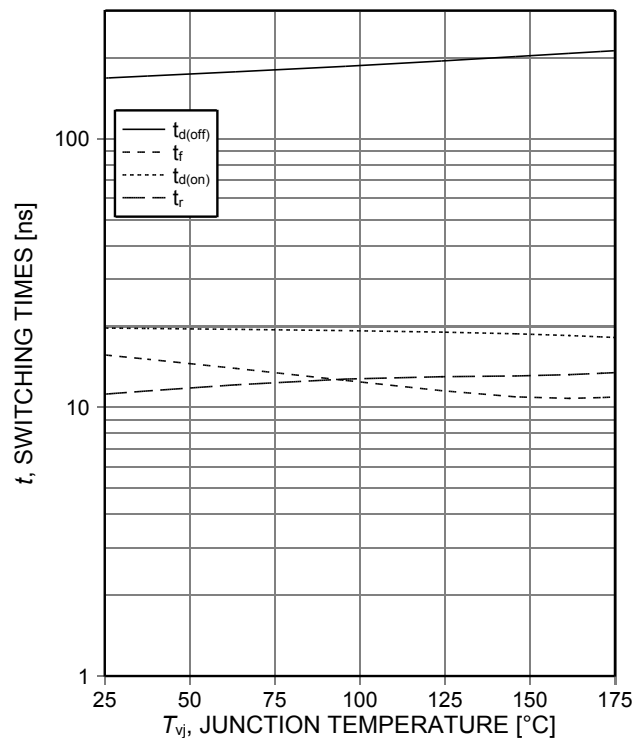


Figure 10. **Typical switching times as a function of junction temperature**
(inductive load, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_C=10\text{A}$, $r_G=32\Omega$, Dynamic test circuit in Figure E)

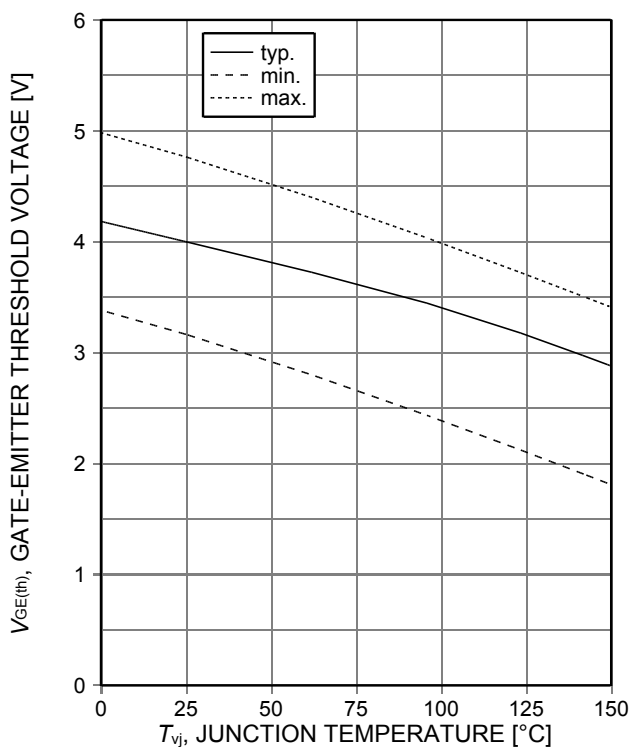


Figure 11. **Gate-emitter threshold voltage as a function of junction temperature**
($I_C=0.2\text{mA}$)

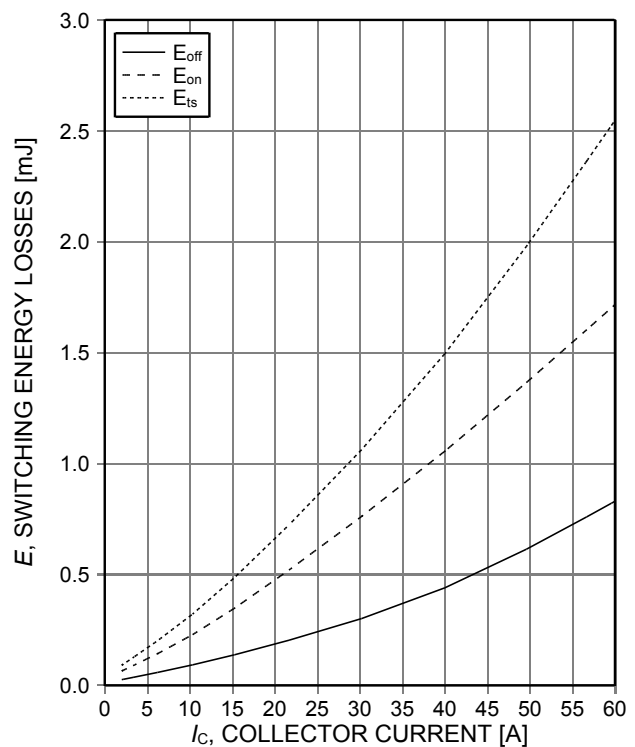


Figure 12. **Typical switching energy losses as a function of collector current**
(inductive load, $T_{vj}=150^{\circ}\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $r_G=32\Omega$, Dynamic test circuit in Figure E)

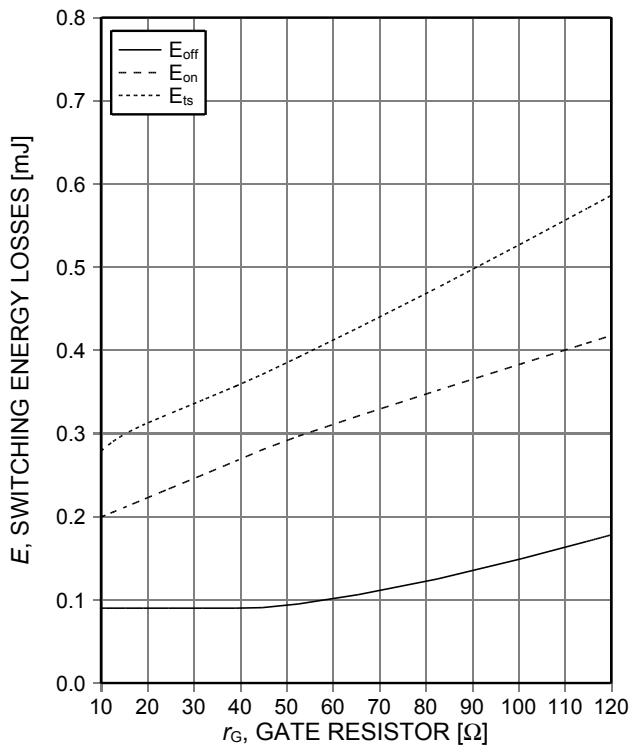


Figure 13. **Typical switching energy losses as a function of gate resistor**
(inductive load, $T_{vj}=150^{\circ}\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_C=10\text{A}$, Dynamic test circuit in Figure E)

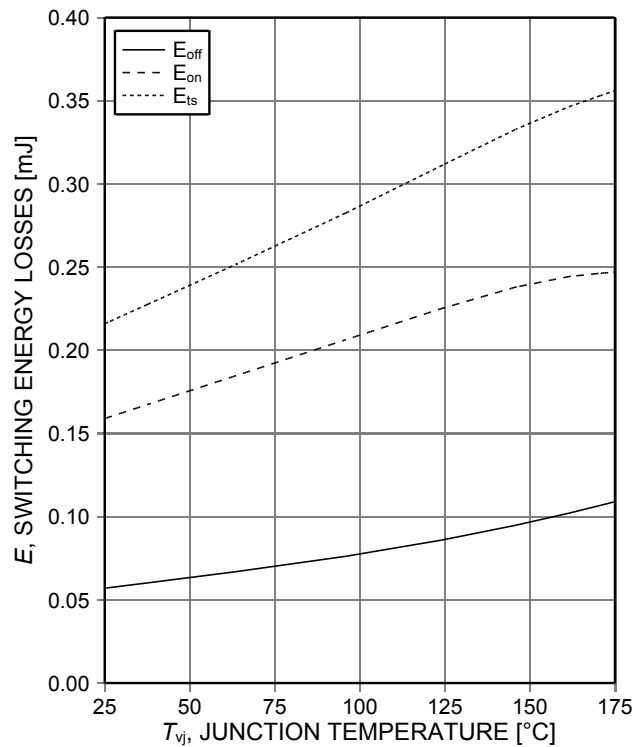


Figure 14. **Typical switching energy losses as a function of junction temperature**
(inductive load, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_C=10\text{A}$, $r_G=32\Omega$, Dynamic test circuit in Figure E)

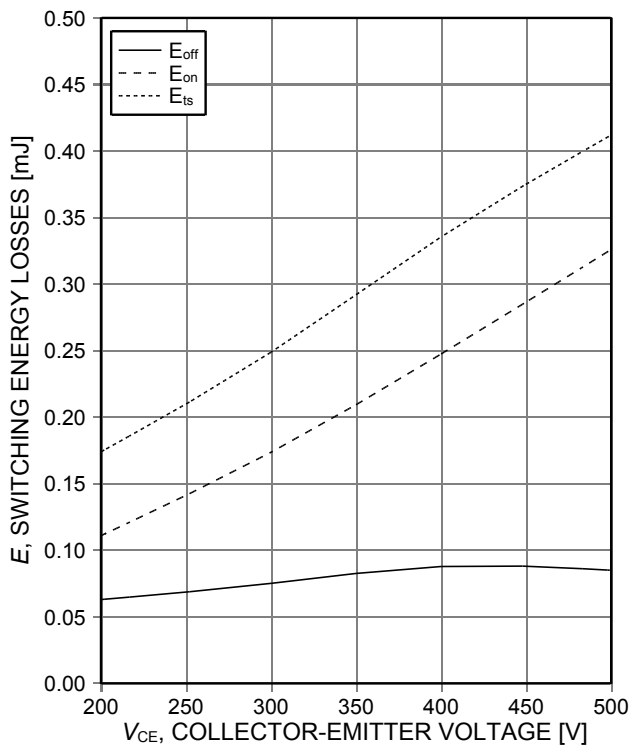


Figure 15. **Typical switching energy losses as a function of collector emitter voltage**
(inductive load, $T_{vj}=150^{\circ}\text{C}$, $V_{GE}=15/0\text{V}$, $I_C=10\text{A}$, $r_G=32\Omega$, Dynamic test circuit in Figure E)

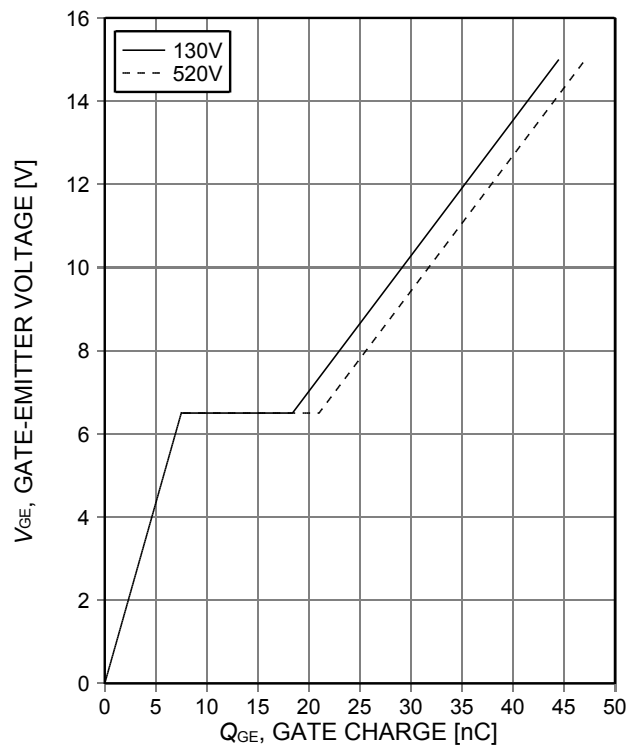


Figure 16. **Typical gate charge**
($I_C=20\text{A}$)

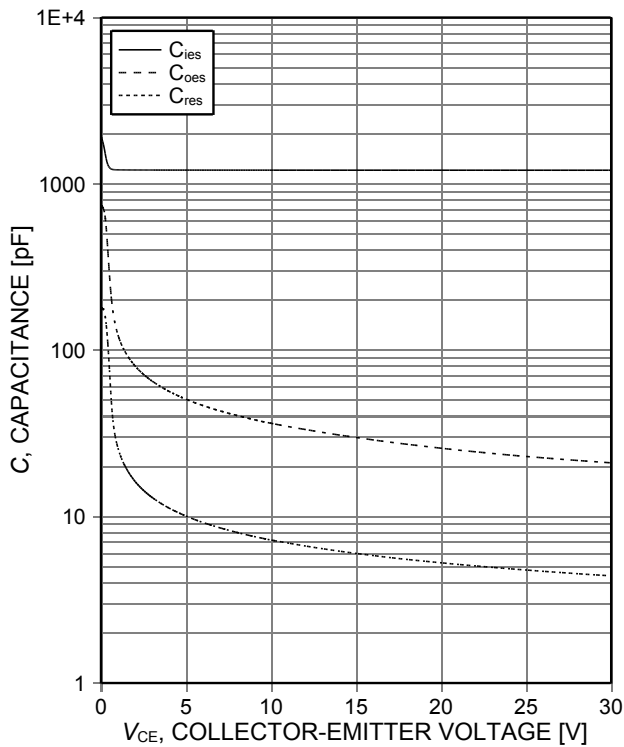


Figure 17. Typical capacitance as a function of collector-emitter voltage ($V_{GE}=0V$, $f=1MHz$)

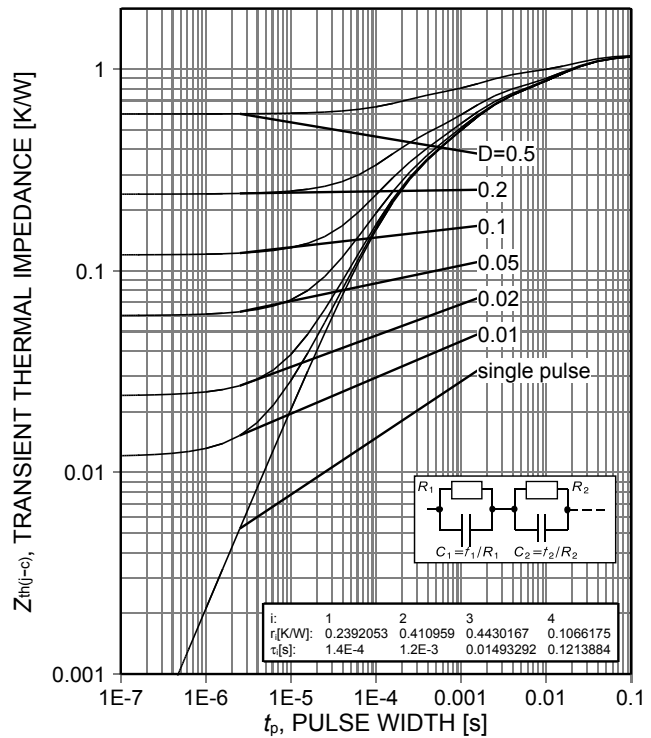


Figure 18. IGBT transient thermal impedance ($D=t_p/T$)

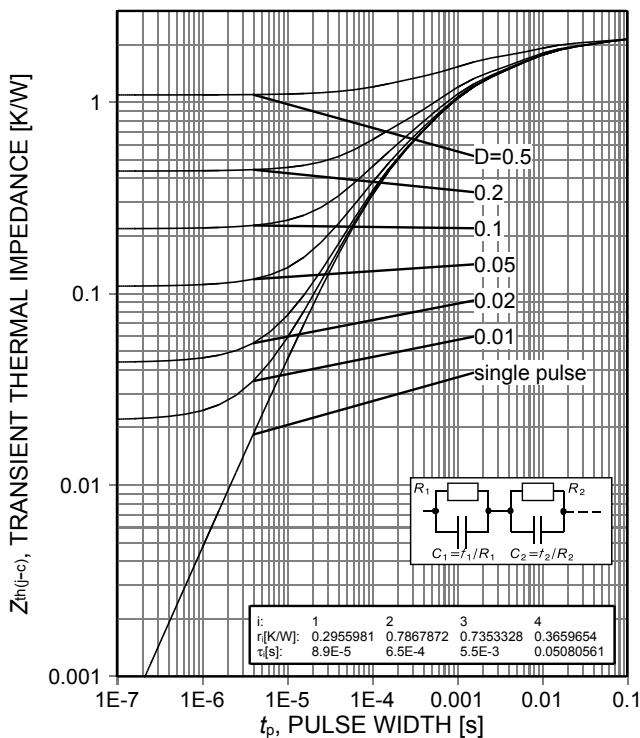


Figure 19. Diode transient thermal impedance as a function of pulse width ($D=t_p/T$)

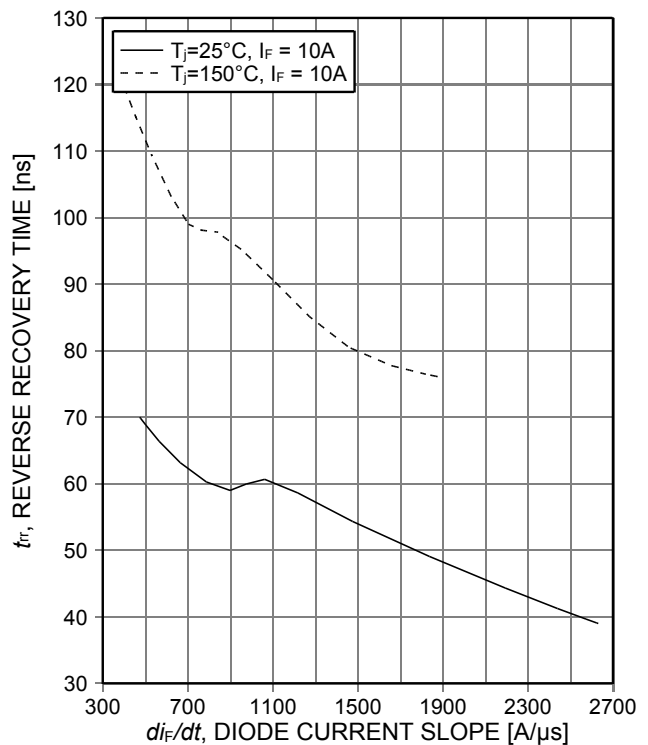


Figure 20. Typical reverse recovery time as a function of diode current slope ($V_R=400V$)

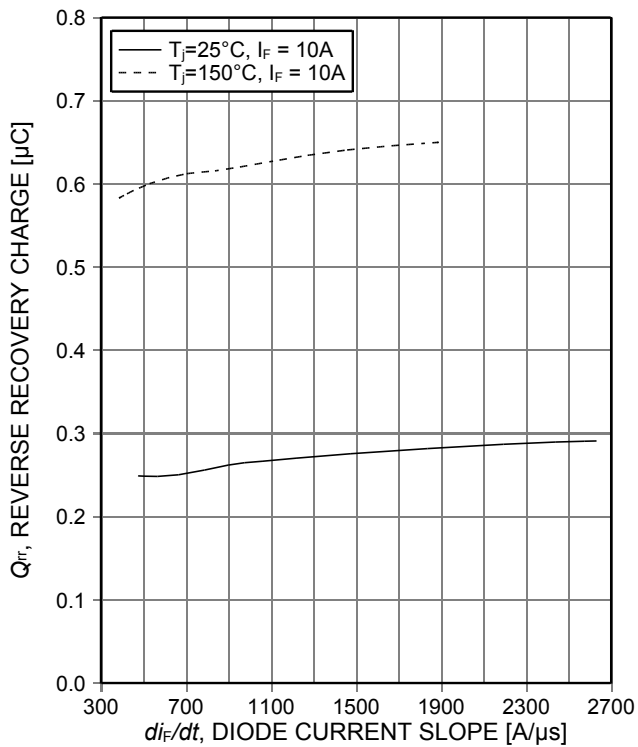


Figure 21. Typical reverse recovery charge as a function of diode current slope ($V_R=400V$)

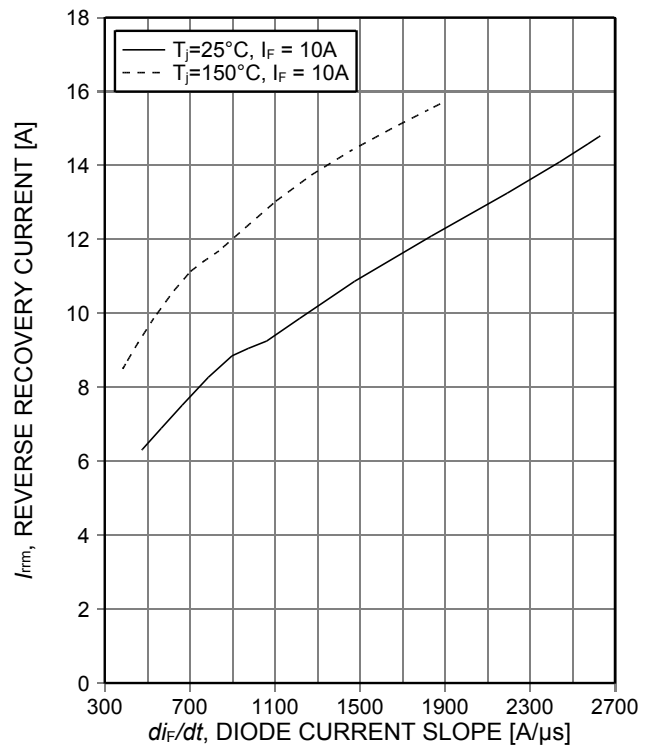


Figure 22. Typical peak reverse recovery current as a function of diode current slope ($V_R=400V$)

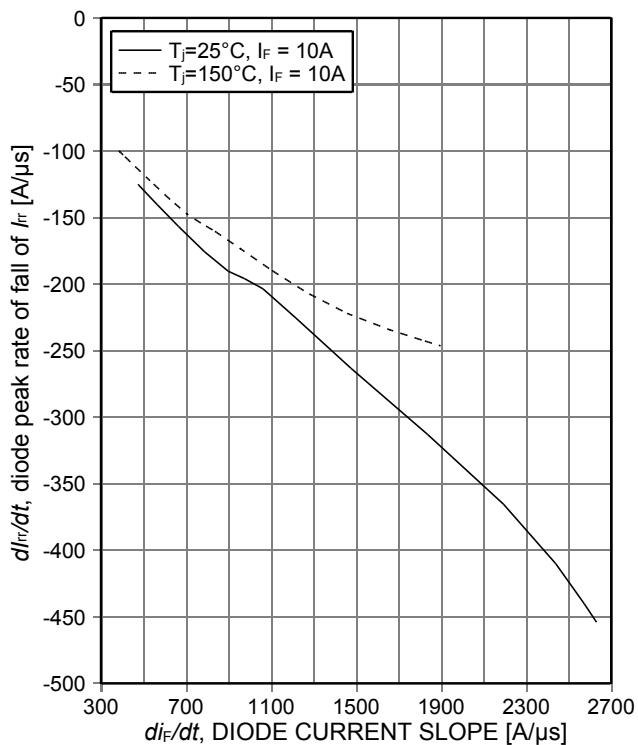


Figure 23. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope ($V_R=400V$)

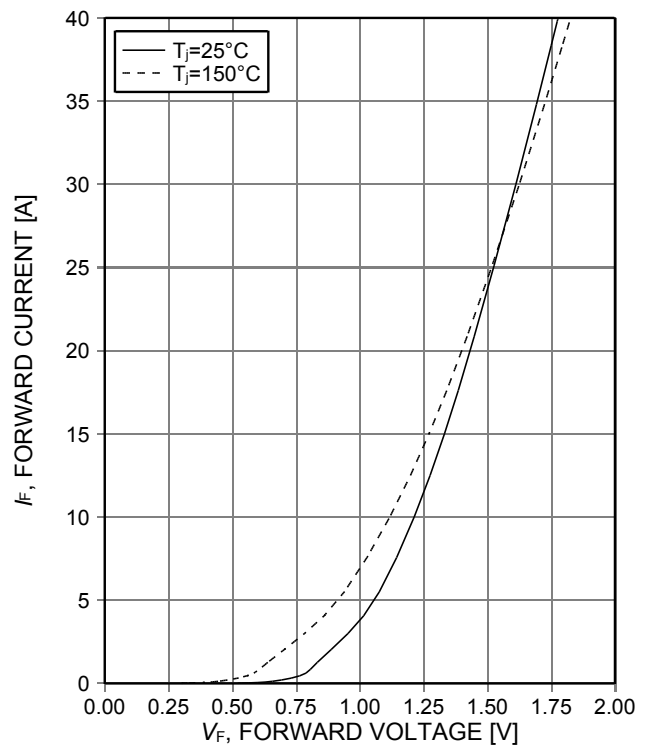


Figure 24. Typical diode forward current as a function of forward voltage

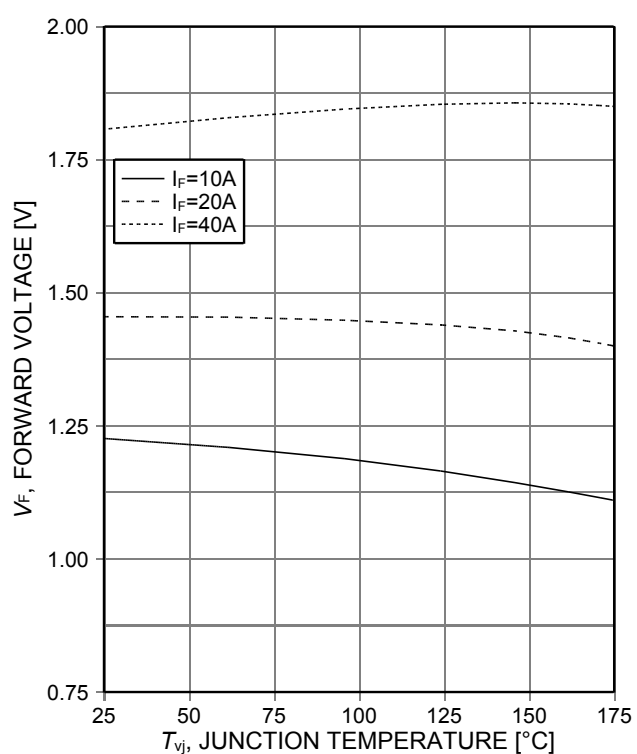
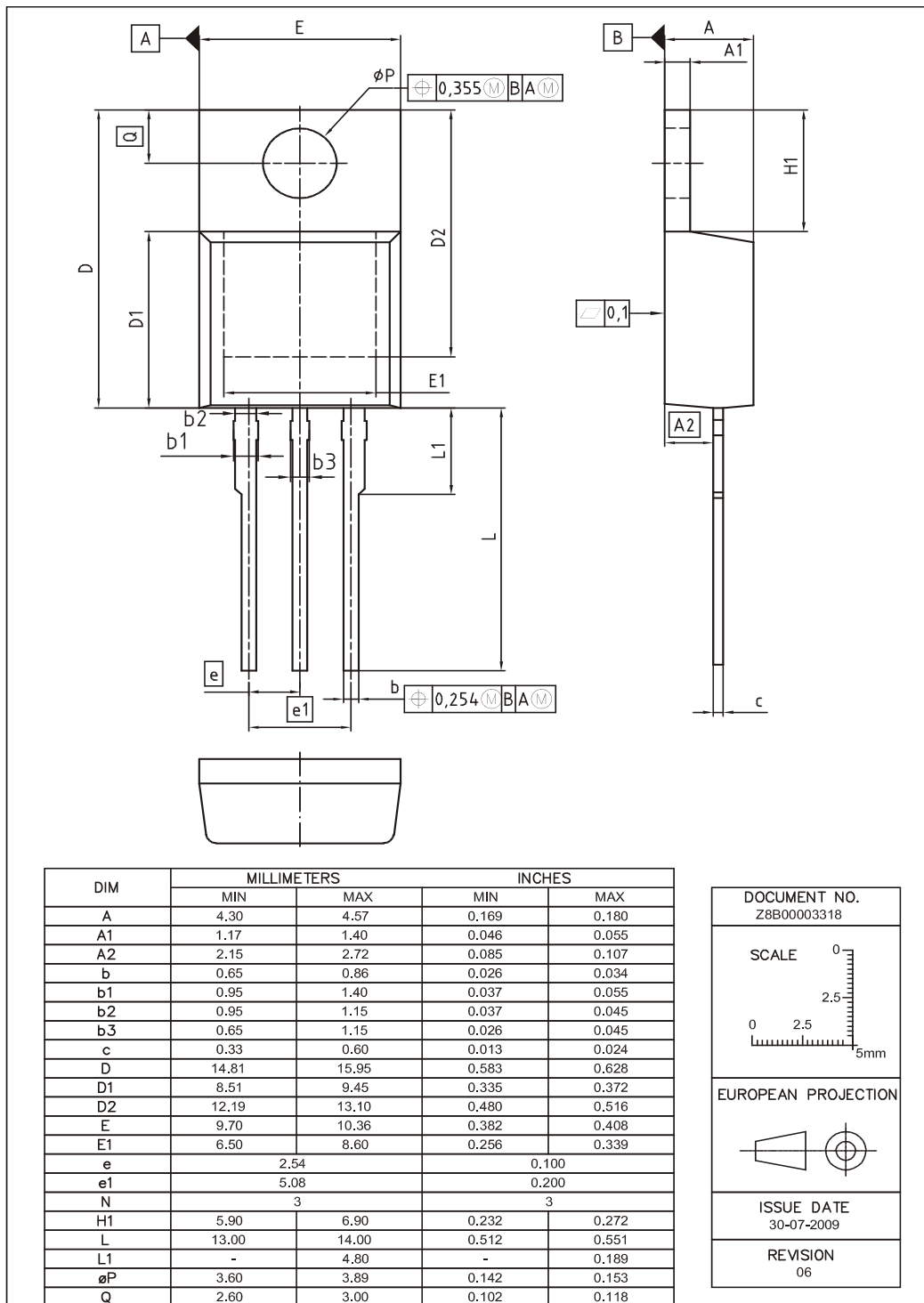


Figure 25. **Typical diode forward voltage as a function of junction temperature**

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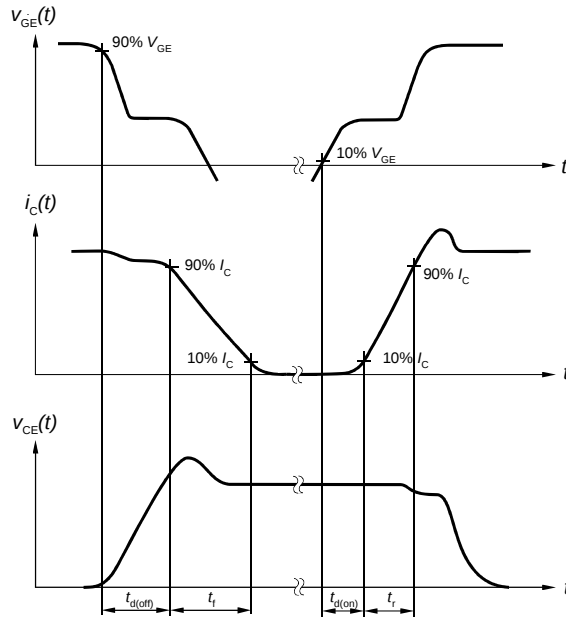


Figure A. Definition of switching times

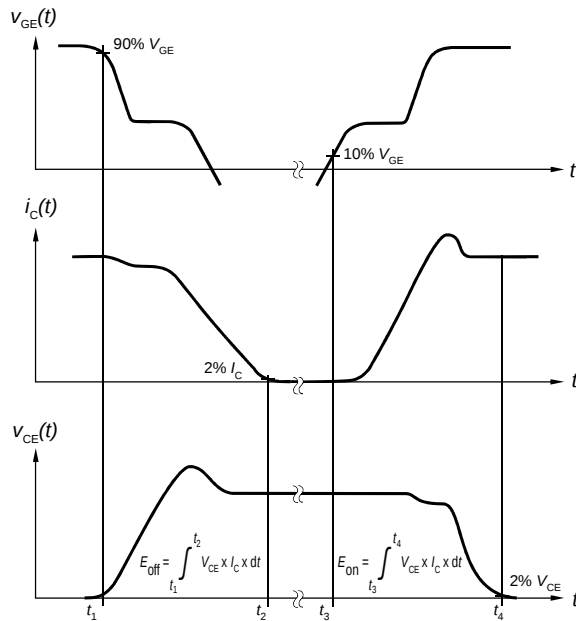


Figure B. Definition of switching losses

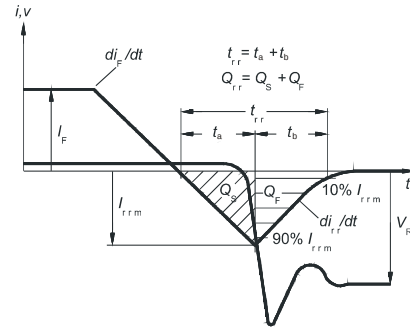


Figure C. Definition of diodes switching characteristics

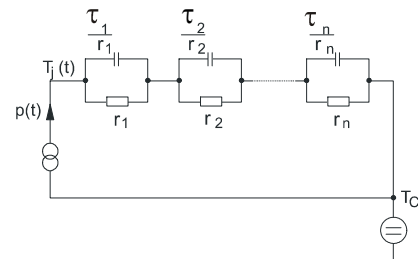
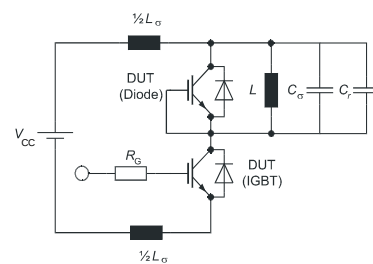


Figure D. Thermal equivalent circuit


Figure E. Dynamic test circuit
Parasitic inductance L_{σ} ,
Parasitic capacitor C_{σ} ,
Relief capacitor C_r
(only for ZVT switching)

Revision History

IKP20N65F5

Revision: 2014-06-11, Rev. 2.1

Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.1	2014-06-11	Final data sheet

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