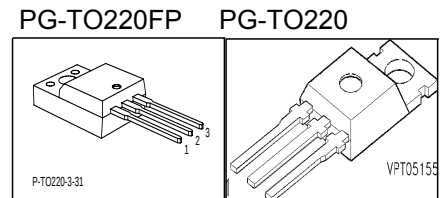


Cool MOS™ Power Transistor

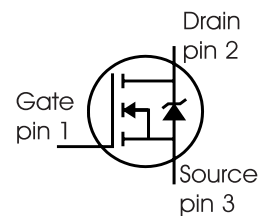
Feature

- New revolutionary high voltage technology
- Ultra low gate charge
- Periodic avalanche rated
- Extreme dv/dt rated
- High peak current capability
- Improved transconductance
- PG-TO-220-3-31;-3-111: Fully isolated package (2500 VAC; 1 minute)
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC⁰⁾ for target applications

$V_{DS} @ T_{jmax}$	650	V
$R_{DS(on)}$	0.95	Ω
I_D	4.5	A



Type	Package	Ordering Code	Marking
SPP04N60C3	PG-TO220	Q67040-S4366	04N60C3
SPA04N60C3	PG-TO220FP	SP000216299	04N60C3



Maximum Ratings

Parameter	Symbol	Value		Unit
		SPP	SPA	
Continuous drain current $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	I_D	4.5 2.8	4.5 ¹⁾ 2.8 ¹⁾	A
Pulsed drain current, t_p limited by T_{jmax}	$I_{D \text{ puls}}$	13.5	13.5	A
Avalanche energy, single pulse $I_D=3.4, V_{DD}=50\text{V}$	E_{AS}	130	130	mJ
Avalanche energy, repetitive t_{AR} limited by T_{jmax} ²⁾ $I_D=4.5\text{A}, V_{DD}=50\text{V}$	E_{AR}	0.4	0.4	
Avalanche current, repetitive t_{AR} limited by T_{jmax}	I_{AR}	4.5	4.5	A
Gate source voltage static	V_{GS}	± 20	± 20	V
Gate source voltage AC ($f > 1\text{Hz}$)	V_{GS}	± 30	± 30	
Power dissipation, $T_C = 25^\circ\text{C}$	P_{tot}	50	31	W
Operating and storage temperature	T_j, T_{stg}	-55...+150		$^\circ\text{C}$
Reverse diode dv/dt ⁷⁾	dv/dt	15		V/ns

Maximum Ratings

Parameter	Symbol	Value	Unit
Drain Source voltage slope $V_{DS} = 480 \text{ V}$, $I_D = 4.5 \text{ A}$, $T_j = 125^\circ\text{C}$	dv/dt	50	V/ns

Thermal Characteristics

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Thermal resistance, junction - case	R_{thJC}	-	-	2.5	K/W
Thermal resistance, junction - case, FullPAK	$R_{thJC \text{ FP}}$	-	-	4	
Thermal resistance, junction - ambient, leaded	R_{thJA}	-	-	62	
Thermal resistance, junction - ambient, FullPAK	$R_{thJA \text{ FP}}$	-	-	80	
SMD version, device on PCB: @ min. footprint @ 6 cm ² cooling area ³⁾	R_{thJA}	-	-	62	
		-	35	-	
Soldering temperature, wavesoldering 1.6 mm (0.063 in.) from case for 10s ⁴⁾	T_{sold}	-	-	260	°C

Electrical Characteristics, at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}$, $I_D=0.25\text{mA}$	600	-	-	V
Drain-Source avalanche breakdown voltage	$V_{(BR)DS}$	$V_{GS}=0\text{V}$, $I_D=4.5\text{A}$	-	700	-	
Gate threshold voltage	$V_{GS(th)}$	$I_D=200\mu\text{A}$, $V_{GS}=V_{DS}$	2.1	3	3.9	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=600\text{V}$, $V_{GS}=0\text{V}$, $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	-	0.5	1	μA
			-	-	50	
Gate-source leakage current	I_{GSS}	$V_{GS}=30\text{V}$, $V_{DS}=0\text{V}$	-	-	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}$, $I_D=2.8\text{A}$, $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	-	0.85	0.95	Ω
			-	2.3	-	
Gate input resistance	R_G	$f=1\text{MHz}$, open drain	-	0.95	-	

Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Transconductance	g_{fs}	$V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$, $I_D = 2.8A$	-	4.4	-	S
Input capacitance	C_{iss}	$V_{GS} = 0V$, $V_{DS} = 25V$, $f = 1MHz$	-	490	-	pF
Output capacitance	C_{oss}		-	160	-	
Reverse transfer capacitance	C_{rss}		-	15	-	
Effective output capacitance, ⁵⁾ energy related	$C_{o(er)}$	$V_{GS} = 0V$, $V_{DS} = 0V$ to $480V$	-	20	-	
Effective output capacitance, ⁶⁾ time related	$C_{o(tr)}$		-	35	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 380V$, $V_{GS} = 0/10V$, $I_D = 4.5A$, $R_G = 18\Omega$	-	6	-	ns
Rise time	t_r		-	2.5	-	
Turn-off delay time	$t_{d(off)}$		-	58.5	80	
Fall time	t_f		-	9.5	14	

Gate Charge Characteristics

Gate to source charge	Q_{gs}	$V_{DD} = 480V$, $I_D = 4.5A$	-	2.2	-	nC
Gate to drain charge	Q_{gd}		-	8.8	-	
Gate charge total	Q_g	$V_{DD} = 480V$, $I_D = 4.5A$, $V_{GS} = 0$ to $10V$	-	19	25	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 480V$, $I_D = 4.5A$	-	5	-	V

⁰J-STD20 and JESD22

¹Limited only by maximum temperature

²Repetitive avalanche causes additional power losses that can be calculated as $P_{AV} = E_{AR} \cdot f$.

³Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 µm thick) copper area for drain connection. PCB is vertical without blown air.

⁴Soldering temperature for TO-263: 220°C, reflow

⁵ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

⁶ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

⁷ $I_{SD} \leq I_D$, $di/dt \leq 400A/\mu s$, $V_{DClamp} = 400V$, $V_{peak} < V_{BR, DSS}$, $T_j < T_{j,max}$.

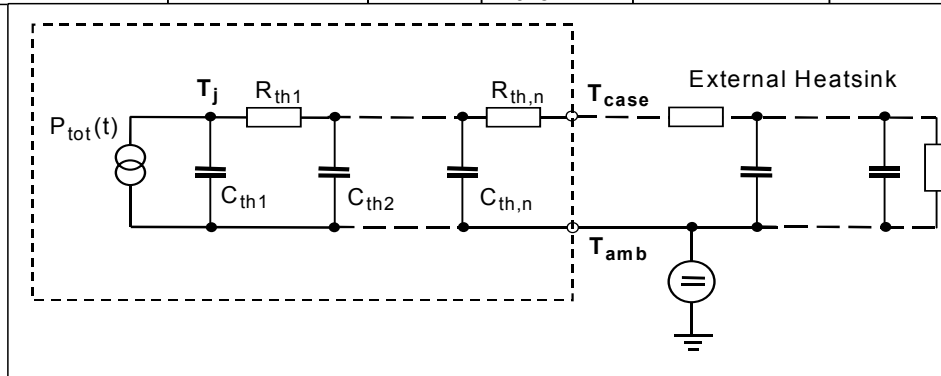
Identical low-side and high-side switch.

Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Inverse diode continuous forward current	I_S	$T_C=25^\circ\text{C}$	-	-	4.5	A
Inverse diode direct current, pulsed	I_{SM}		-	-	13.5	
Inverse diode forward voltage	V_{SD}	$V_{GS}=0\text{V}, I_F=I_S$	-	1	1.2	V
Reverse recovery time	t_{rr}	$V_R=480\text{V}, I_F=I_S, di_F/dt=100\text{A}/\mu\text{s}$	-	300	500	ns
Reverse recovery charge	Q_{rr}		-	2.6	-	μC
Peak reverse recovery current	I_{rrm}		-	18	-	A
Peak rate of fall of reverse recovery current	di_{rr}/dt	$T_j=25^\circ\text{C}$	-	900	-	$\text{A}/\mu\text{s}$

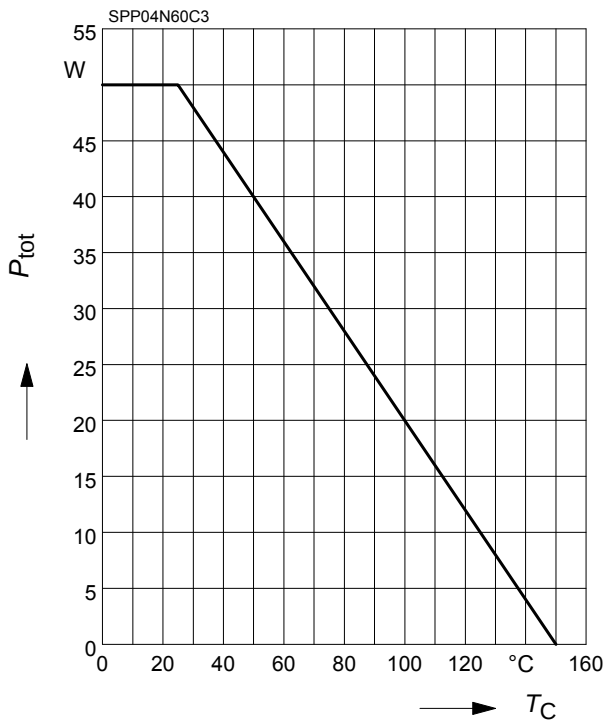
Typical Transient Thermal Characteristics

Symbol	Value		Unit	Symbol	Value		Unit
	SPP	SPA			SPP	SPA	
R_{th1}	0.039	0.039	K/W	C_{th1}	0.00007347	0.00007347	Ws/K
R_{th2}	0.074	0.074		C_{th2}	0.0002831	0.0002831	
R_{th3}	0.132	0.132		C_{th3}	0.0004062	0.0004062	
R_{th4}	0.555	0.272		C_{th4}	0.001215	0.001215	
R_{th5}	0.529	0.559		C_{th5}	0.00276	0.005633	
R_{th6}	0.169	2.523		C_{th6}	0.029	0.412	



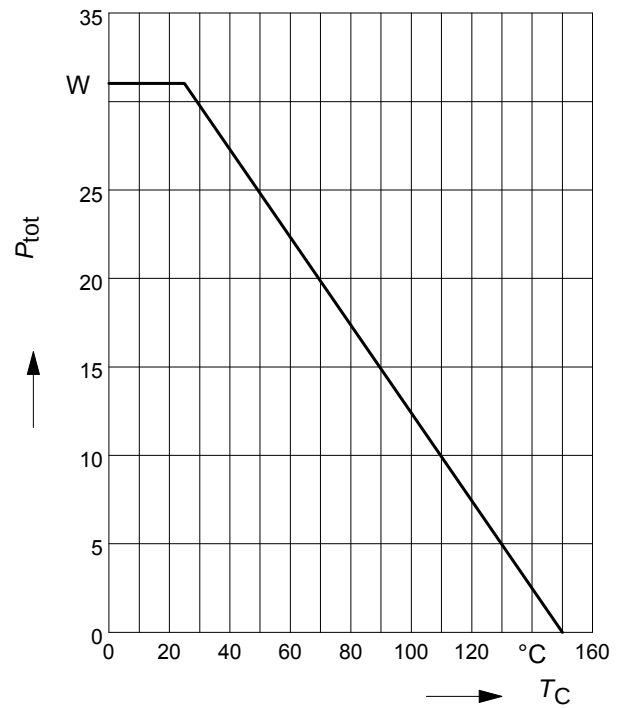
1 Power dissipation

$$P_{\text{tot}} = f(T_C)$$



2 Power dissipation FullPAK

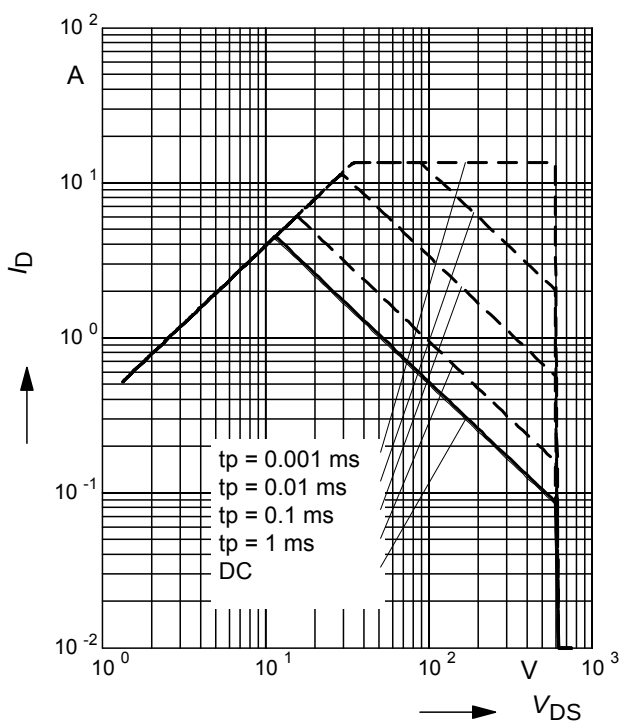
$$P_{\text{tot}} = f(T_C)$$



3 Safe operating area

$$I_D = f(V_{DS})$$

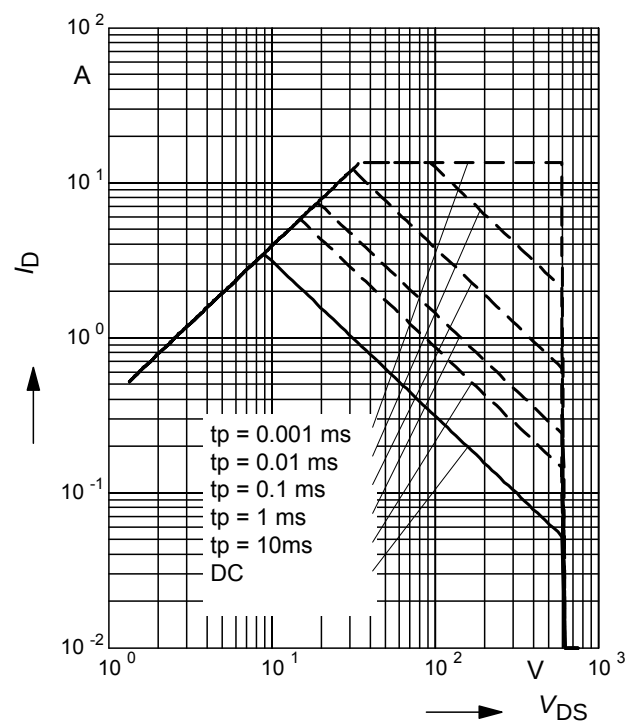
parameter : $D = 0$, $T_C = 25^\circ\text{C}$



4 Safe operating area FullPAK

$$I_D = f(V_{DS})$$

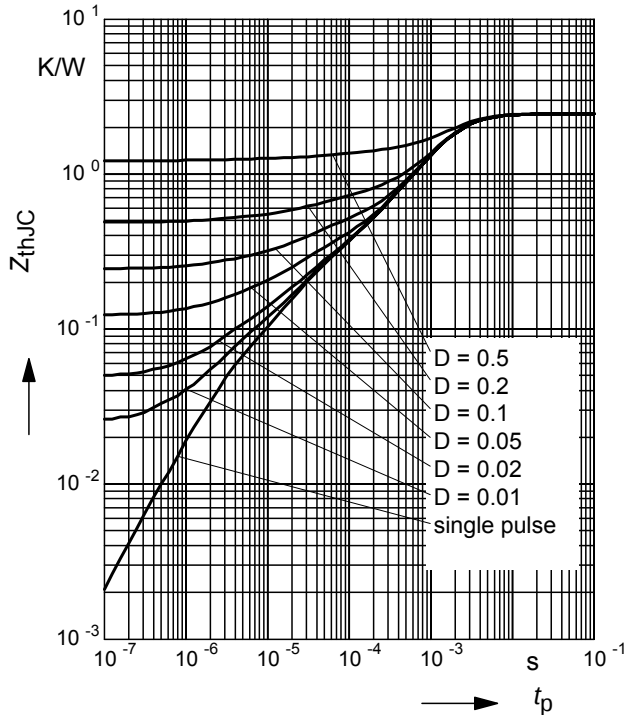
parameter: $D = 0$, $T_C = 25^\circ\text{C}$



5 Transient thermal impedance

$$Z_{thJC} = f(t_p)$$

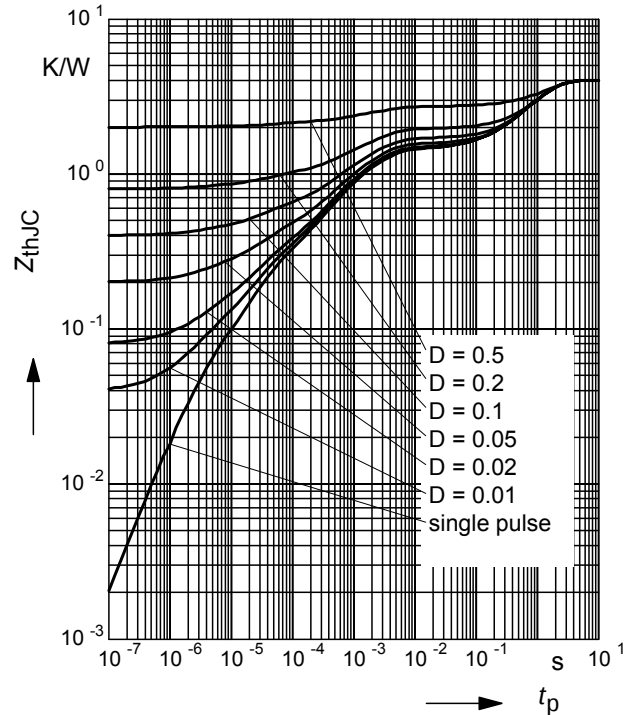
parameter: $D = t_p/T$



6 Transient thermal impedance FullPAK

$$Z_{thJC} = f(t_p)$$

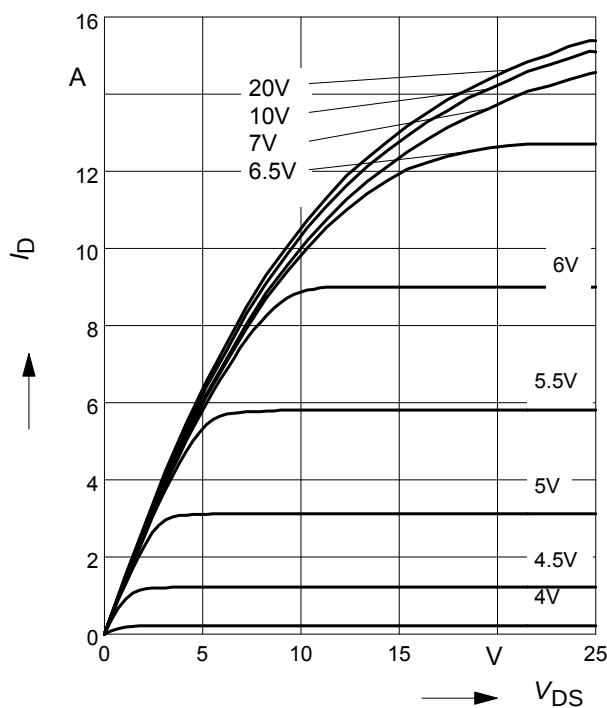
parameter: $D = t_p/t$



7 Typ. output characteristic

$$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$$

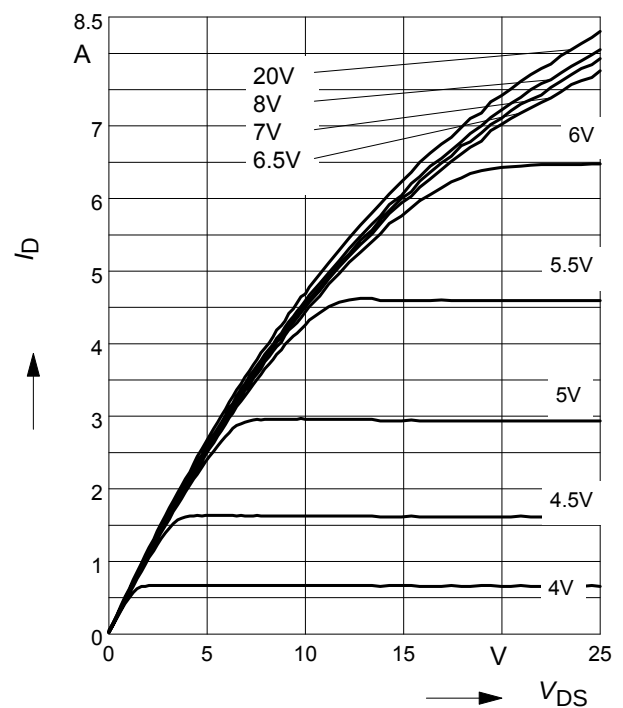
parameter: $t_p = 10 \mu\text{s}, V_{GS}$



8 Typ. output characteristic

$$I_D = f(V_{DS}); T_j = 150^\circ\text{C}$$

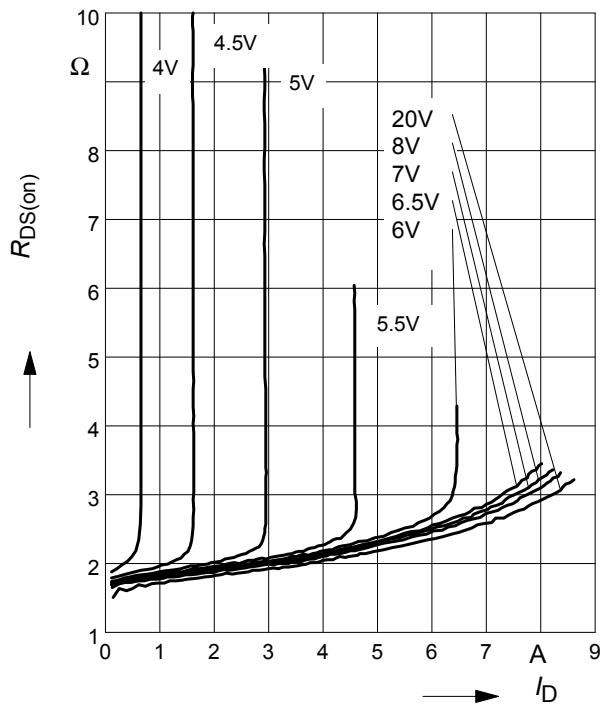
parameter: $t_p = 10 \mu\text{s}, V_{GS}$



9 Typ. drain-source on resistance

$$R_{DS(on)} = f(I_D)$$

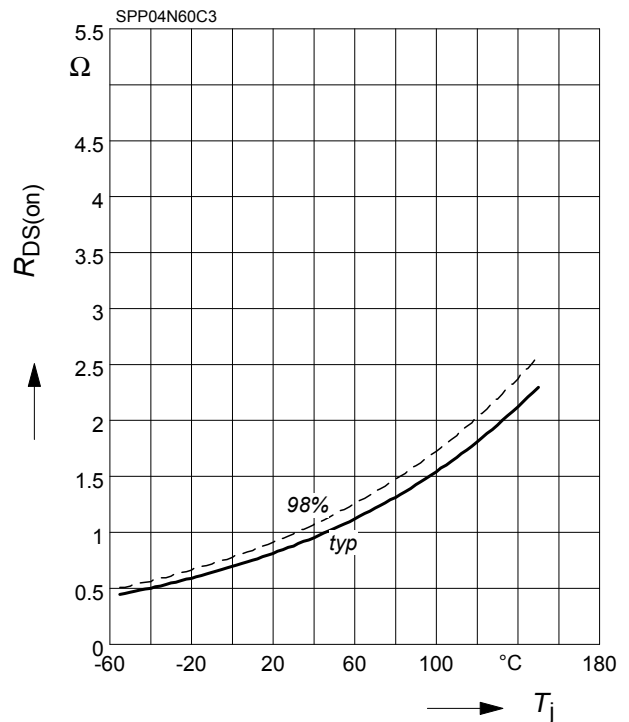
parameter: $T_j = 150^\circ\text{C}$, V_{GS}



10 Drain-source on-state resistance

$$R_{DS(on)} = f(T_j)$$

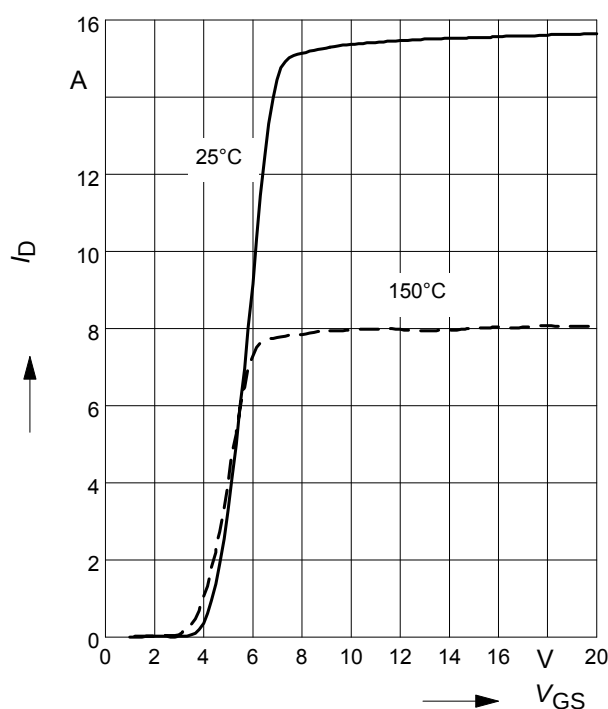
parameter: $I_D = 2.8\text{ A}$, $V_{GS} = 10\text{ V}$



11 Typ. transfer characteristics

$$I_D = f(V_{GS}); V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$$

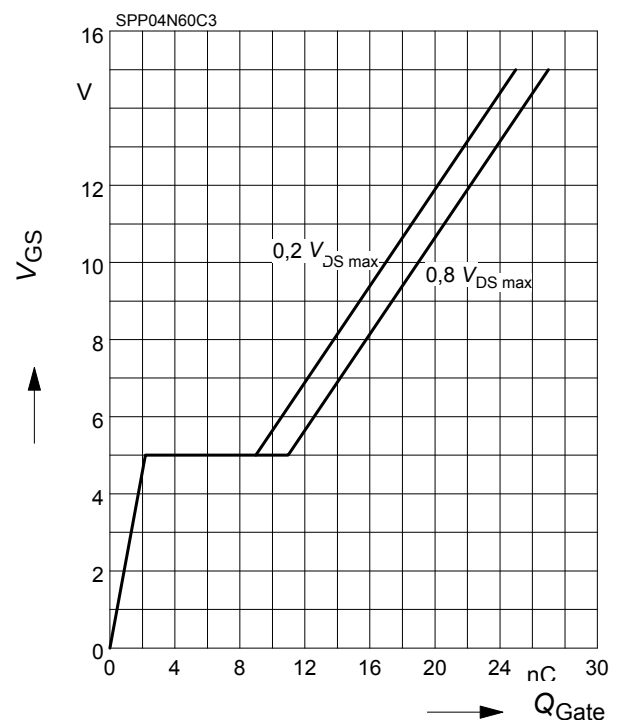
parameter: $t_p = 10\text{ }\mu\text{s}$



12 Typ. gate charge

$$V_{GS} = f(Q_{Gate})$$

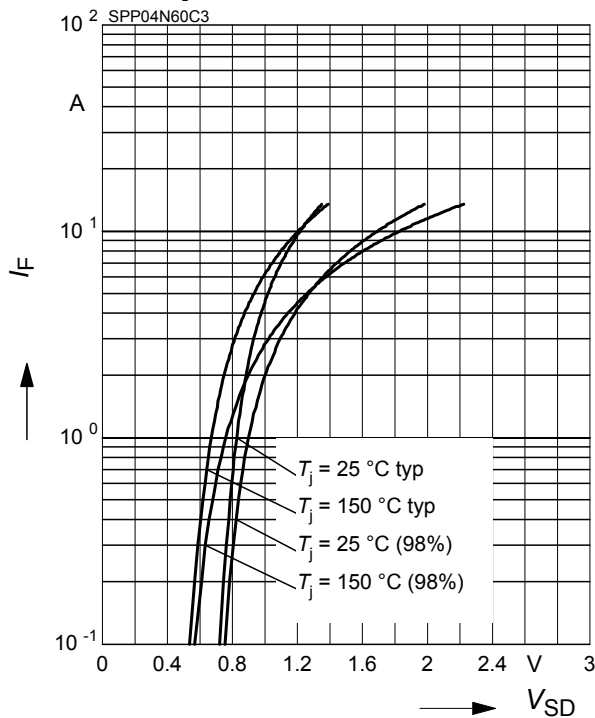
parameter: $I_D = 4.5\text{ A}$ pulsed



13 Forward characteristics of body diode

$$I_F = f(V_{SD})$$

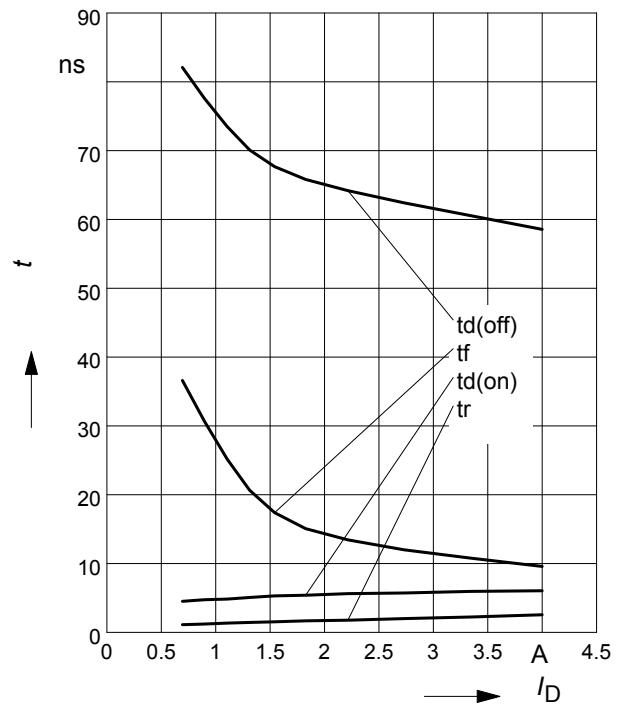
parameter: T_j , $t_p = 10 \mu s$



14 Typ. switching time

$$t = f(I_D), \text{ inductive load, } T_j = 125^\circ C$$

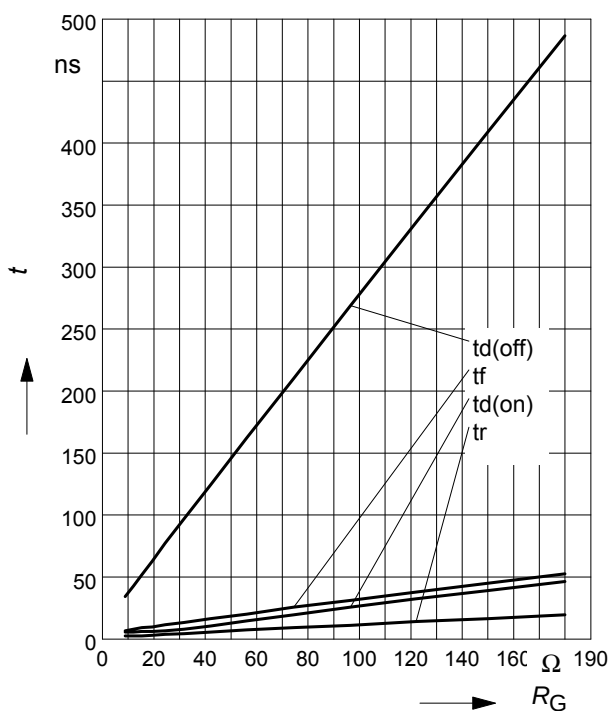
par.: $V_{DS} = 380V$, $V_{GS} = 0/+13V$, $R_G = 18\Omega$



15 Typ. switching time

$$t = f(R_G), \text{ inductive load, } T_j = 125^\circ C$$

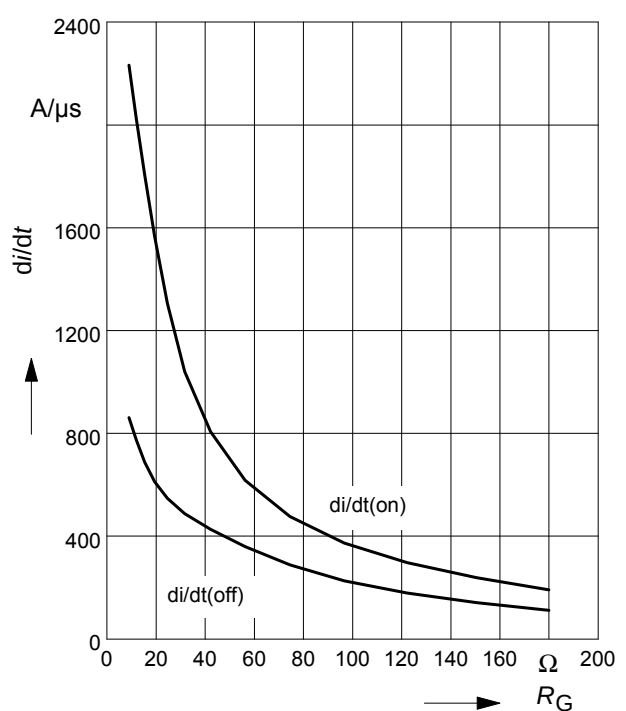
par.: $V_{DS} = 380V$, $V_{GS} = 0/+13V$, $I_D = 4.5 A$



16 Typ. drain current slope

$$di/dt = f(R_G), \text{ inductive load, } T_j = 125^\circ C$$

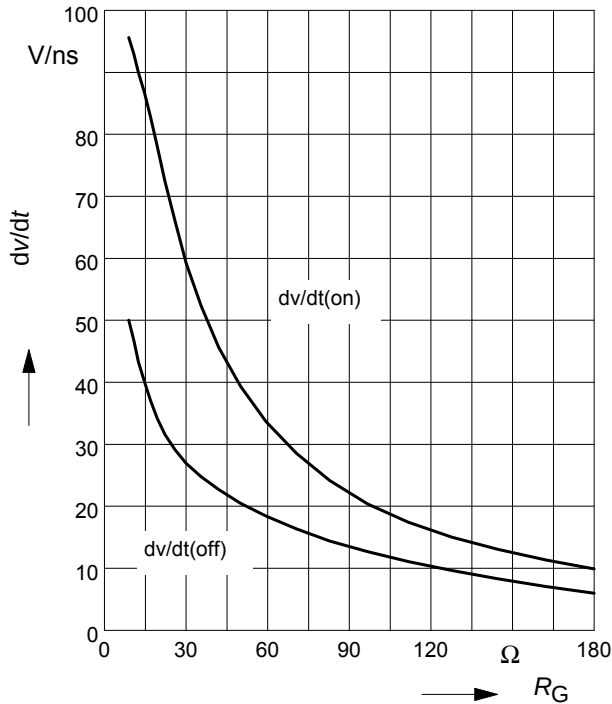
par.: $V_{DS} = 380V$, $V_{GS} = 0/+13V$, $I_D = 4.5A$



17 Typ. drain source voltage slope

$dv/dt = f(R_G)$, inductive load, $T_j = 125^\circ\text{C}$

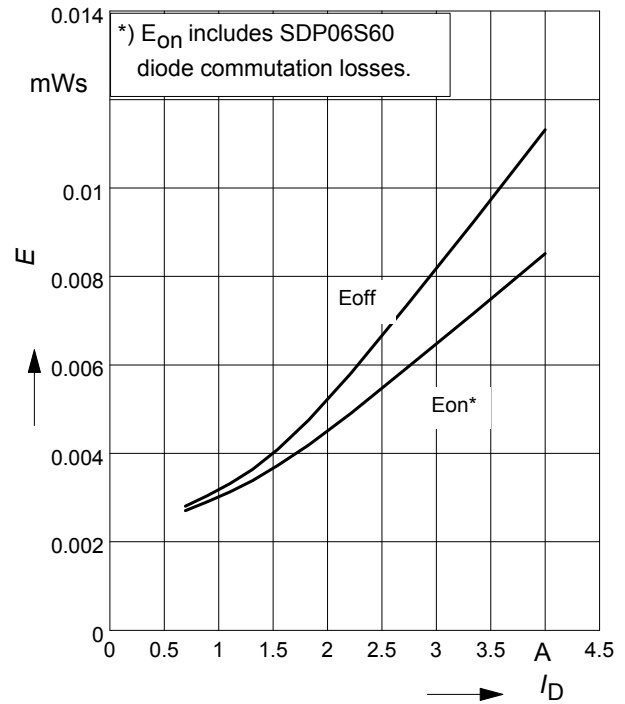
par.: $V_{DS}=380\text{V}$, $V_{GS}=0/+13\text{V}$, $I_D=4.5\text{A}$



18 Typ. switching losses

$E = f(I_D)$, inductive load, $T_j=125^\circ\text{C}$

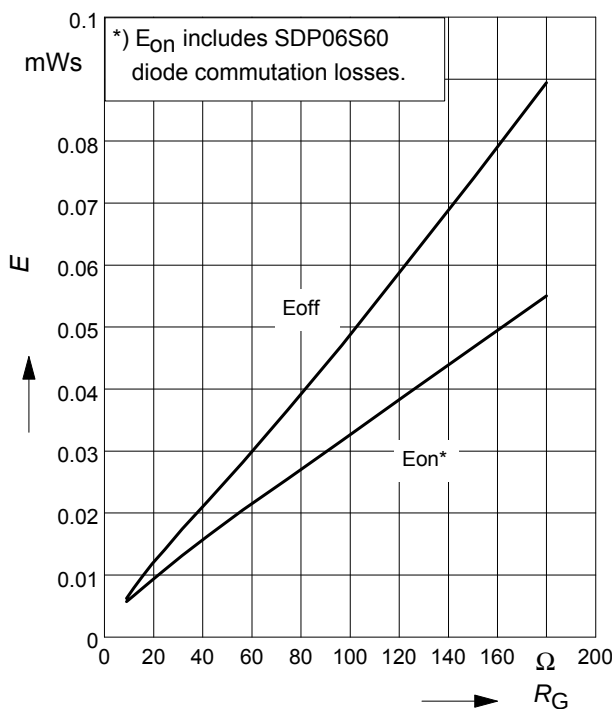
par.: $V_{DS}=380\text{V}$, $V_{GS}=0/+13\text{V}$, $R_G=18\Omega$



19 Typ. switching losses

$E = f(R_G)$, inductive load, $T_j=125^\circ\text{C}$

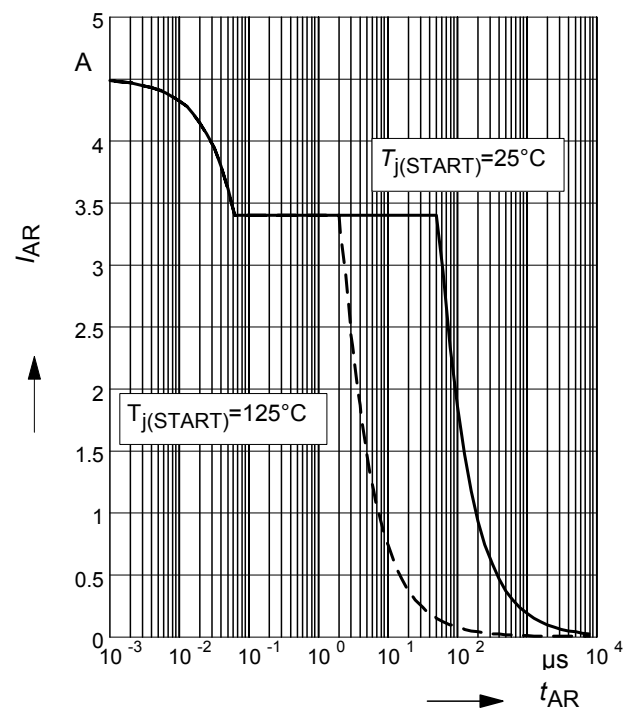
par.: $V_{DS}=380\text{V}$, $V_{GS}=0/+13\text{V}$, $I_D=4.5\text{A}$



20 Avalanche SOA

$I_{AR} = f(t_{AR})$

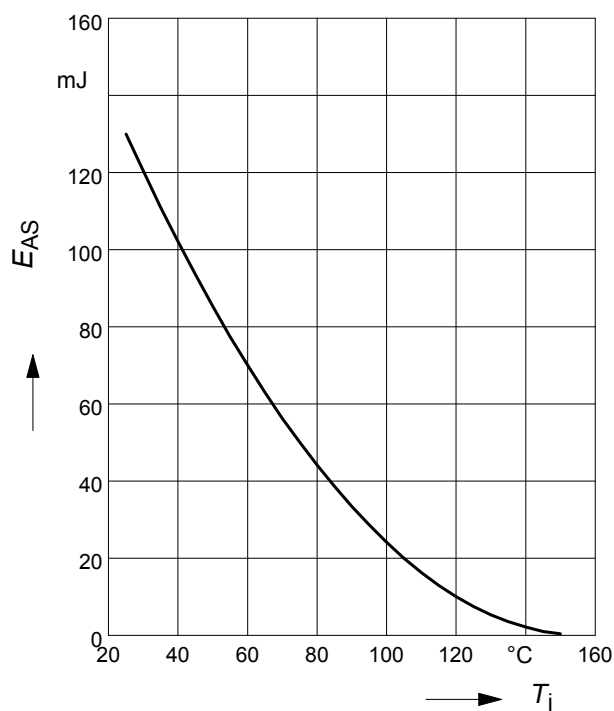
par.: $T_j \leq 150^\circ\text{C}$



21 Avalanche energy

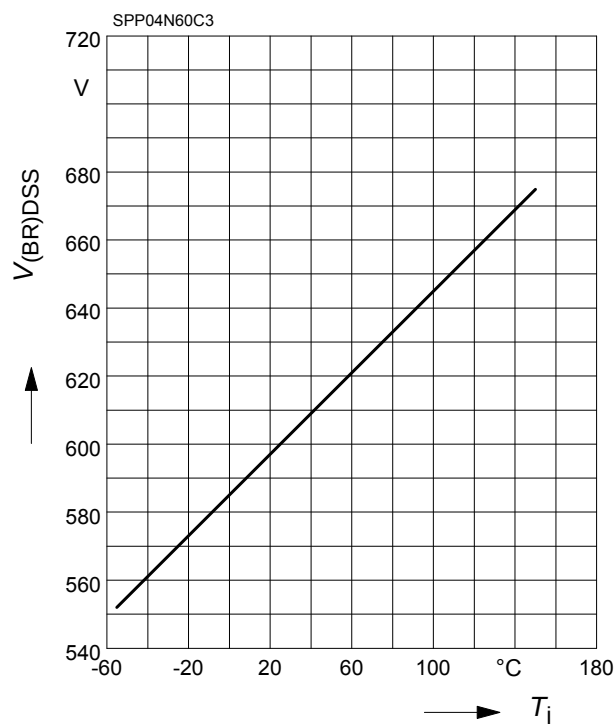
$$E_{AS} = f(T_j)$$

par.: $I_D = 3.4$, $V_{DD} = 50$ V



22 Drain-source breakdown voltage

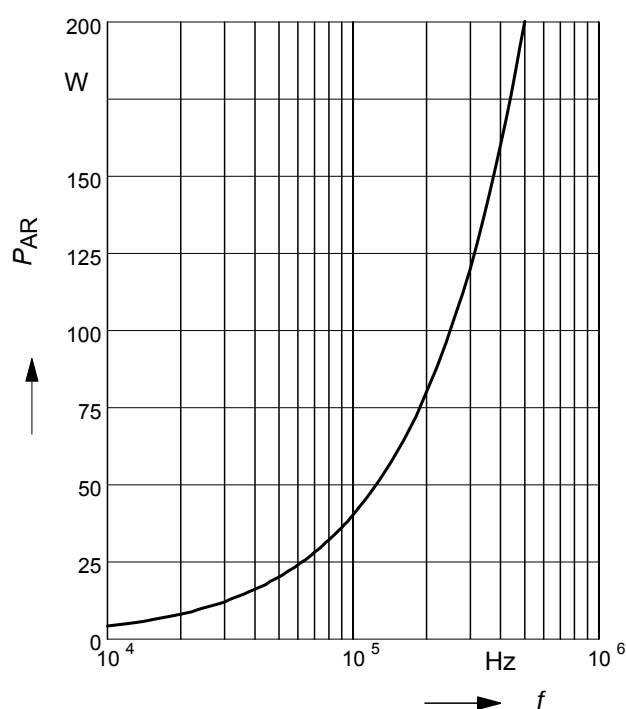
$$V_{(BR)DSS} = f(T_j)$$



23 Avalanche power losses

$$P_{AR} = f(f)$$

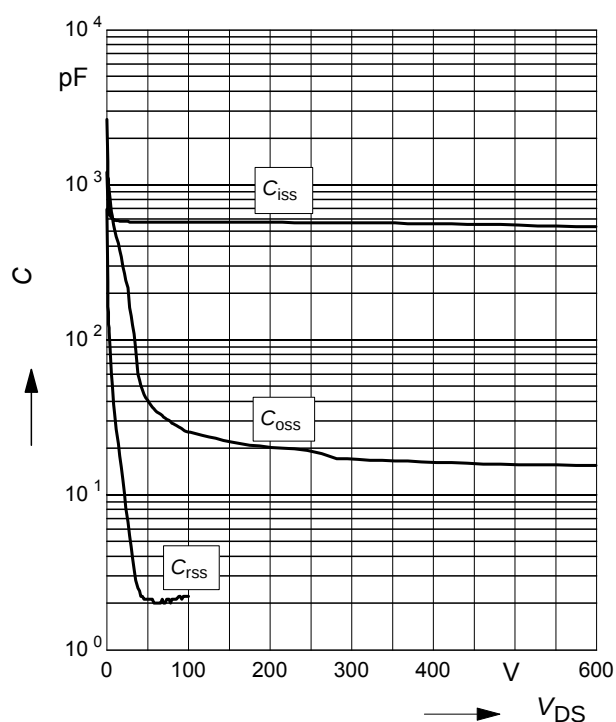
parameter: $E_{AR} = 0.4$ mJ

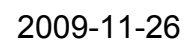


24 Typ. capacitances

$$C = f(V_{DS})$$

parameter: $V_{GS} = 0$ V, $f = 1$ MHz



$$E_{oss}=f(V_{DS})$$


Technical drawing of a mechanical part, showing front, side, and top views with dimensions and tolerances.

Front View (Left):

- Overall width: E
- Overall height: D
- Top flange height: $D1$
- Inner cavity height: $D2$
- Inner cavity width: $E1$
- Top flange thickness: a
- Top flange outer diameter: ϕP
- Top flange tolerance: $\phi 0,355m \text{ BA} \text{ (M)}$
- Bottom flange thickness: $b1$
- Bottom flange outer diameter: $b2$
- Bottom flange inner diameter: $b3$
- Bottom flange tolerance: $\phi 0,254m \text{ BA} \text{ (M)}$
- Bottom flange height: $L1$
- Bottom flange width: b
- Bottom flange tolerance: $\phi 0,254m \text{ BA} \text{ (M)}$
- Bottom flange height: L
- Bottom flange width: e
- Bottom flange tolerance: $\phi 0,254m \text{ BA} \text{ (M)}$
- Bottom flange height: $e1$

Side View (Right):

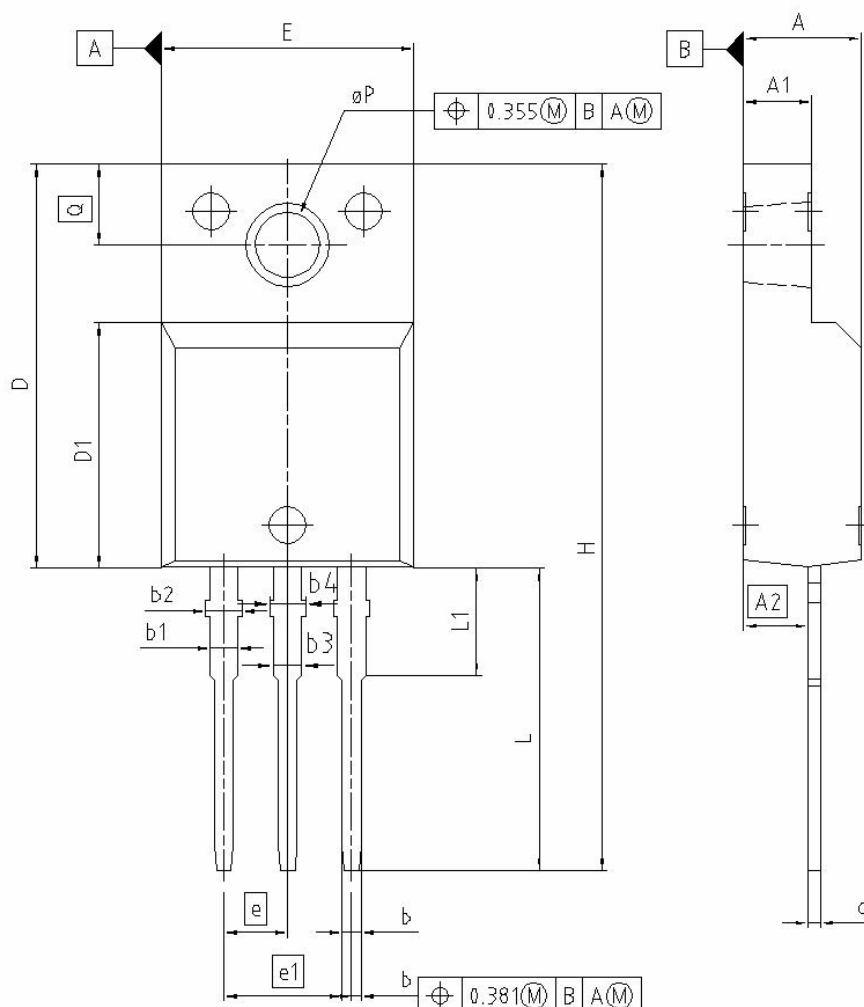
- Overall height: $H1$
- Top flange height: $A1$
- Top flange tolerance: $\phi 0,1$
- Bottom flange height: $A2$
- Bottom flange tolerance: $\phi 0,1$
- Bottom flange width: c

Top View (Bottom):

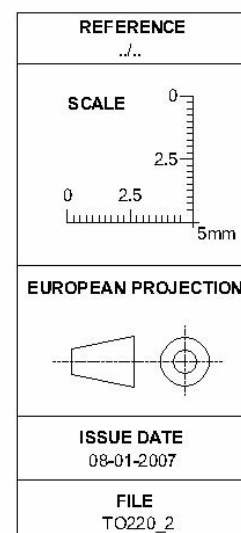
- Overall width: E
- Overall height: D
- Inner cavity width: $E1$
- Inner cavity height: $D2$
- Top flange thickness: a
- Bottom flange thickness: $b1$
- Bottom flange outer diameter: $b2$
- Bottom flange inner diameter: $b3$
- Bottom flange height: $L1$
- Bottom flange width: b
- Bottom flange tolerance: $\phi 0,254m \text{ BA} \text{ (M)}$
- Bottom flange height: L
- Bottom flange width: e
- Bottom flange tolerance: $\phi 0,254m \text{ BA} \text{ (M)}$
- Bottom flange height: $e1$

DOCUMENT NO. Z8B00003318	
SCALE 	
EUROPEAN PROJECTION 	
ISSUE DATE 23-08-2007	
REVISION 05	

PG-TO220-3-31/3-111 Fully isolated package (2500VAC; 1 minute)



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.55	4.85	0.179	0.191
A1	2.55	2.85	0.100	0.112
A2	2.42	2.72	0.095	0.107
b	0.65	0.85	0.026	0.033
b1	0.95	1.33	0.037	0.052
b2	0.95	1.51	0.037	0.059
b3	0.65	1.33	0.026	0.052
b4	0.65	1.51	0.026	0.059
c	0.40	0.63	0.016	0.025
D	15.85	16.15	0.624	0.636
D1	9.53	9.83	0.375	0.387
E	10.35	10.65	0.407	0.419
e	2.54		0.100	
e1	5.08		0.200	
N	3		3	
H	29.45	29.75	1.159	1.171
L	13.45	13.75	0.530	0.541
L1	3.15	3.45	0.124	0.136
øP	2.95	3.20	0.116	0.126
Q	3.15	3.50	0.124	0.138



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