

### Vishay Semiconductors

### Silicon NPN Phototransistor, RoHS Compliant

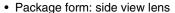


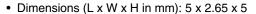
#### **DESCRIPTION**

TEKT5400S is a silicon NPN phototransistor with high radiant sensitivity, molded in a plastic package with side view lens and daylight blocking filter. Filter bandwidth is matched with 950 nm IR emitters.

#### **FEATURES**

• Package type: leaded







Daylight blocking filter matched with 940 nm emitters



ROHS COMPLIANT

- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 37^{\circ}$
- Package matched with IR emitter series TSKS5400S
- Lead (Pb)-free component in accordance with RoHS 2002/95/EC and WEEE 2002/96/EC

#### **APPLICATIONS**

· Detector in electronic control and drive circuits

PRODUCT SUMMARY			
COMPONENT	I <sub>ca</sub> (mA)	φ <b>(deg)</b>	λ <sub>0.5</sub> (nm)
TEKT5400S	4	± 37	850 to 980

#### Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
TEKT5400S	Bulk	MOQ: 2000 pcs, 2000 pcs/bulk	Side view lens	

#### Note

MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		$V_{CEO}$	70	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		I <sub>C</sub>	100	mA	
Collector peak current	$t_p/T \le 0.5,  t_p \le 10 \text{ ms}$	I <sub>CM</sub>	200	mA	
Power dissipation	T <sub>amb</sub> ≤ 40 °C	P <sub>V</sub>	150	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	- 40 to + 85	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C	
Soldering temperature	$t \le 5 s$	$T_{sd}$	260	°C	
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	$R_{thJA}$	270	K/W	

#### Note

 $T_{amb}$  = 25 °C, unless otherwise specified

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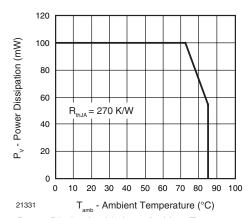


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter voltage	I <sub>C</sub> = 1 mA	$V_{CEO}$	70			V
Emitter collector voltage	I <sub>E</sub> = 100 μA	$V_{ECO}$	7			V
Collector dark current	V <sub>CE</sub> = 20 V, E = 0	I <sub>CEO</sub>		1	100	nA
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz, E = 0	C <sub>CEO</sub>		6		pF
Collector ligth current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm},$ $V_{CE} = 5 \text{ V}$	I <sub>ca</sub>	2	4		mA
Angle of half sensitivity		φ		± 37		deg
Wavelength of peak sensitivity		$\lambda_{p}$		920		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		850 to 980		nm
Collector emitter saturation voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, \\ I_C = 0.1 \text{ mA}$	V <sub>CEsat</sub>			0.3	V
Turn-on time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega$	t <sub>on</sub>		6		μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega$	t <sub>off</sub>		5		μs
Cut-off frequency	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega$	f <sub>c</sub>		110		kHz

#### Note

T<sub>amb</sub> = 25 °C, unless otherwise specified

#### **BASIC CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

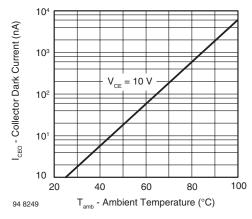


Fig. 2 - Collector Dark Current vs. Ambient Temperature

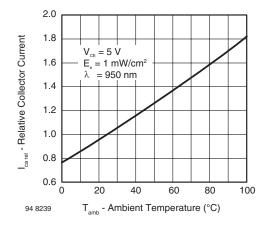


Fig. 3 - Relative Collector Current vs. Ambient Temperature



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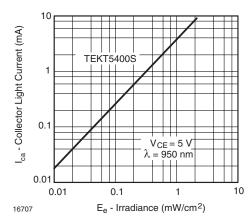


Fig. 4 - Collector Light Current vs. Irradiance

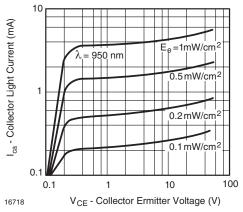


Fig. 5 - Collector Light Current vs. Collector Emitter Voltage

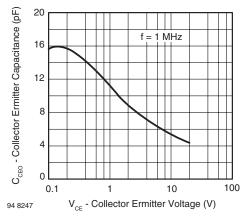


Fig. 6 - Collector Emitter Capacitance vs. Collector Emitter Voltage

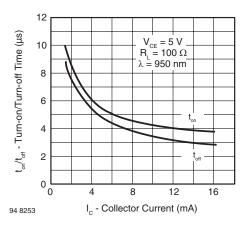


Fig. 7 - Turn-on/Turn-off Time vs. Collector Current

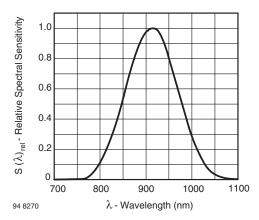


Fig. 8 - Relative Spectral Sensitivity vs. Wavelength

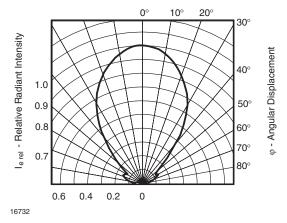
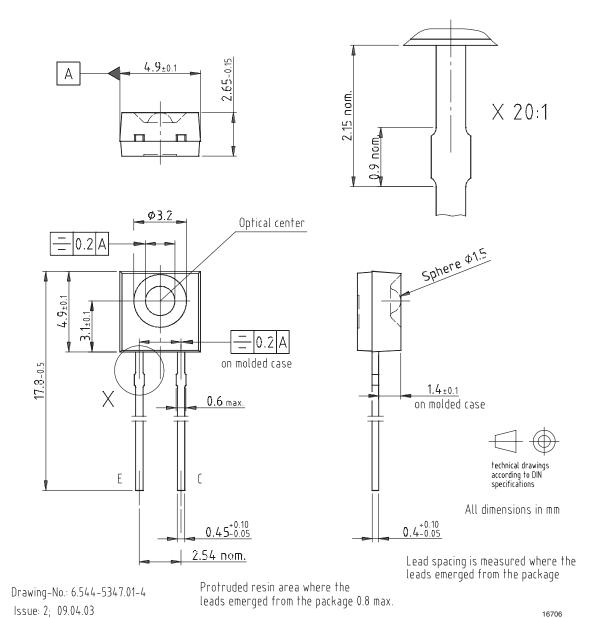


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

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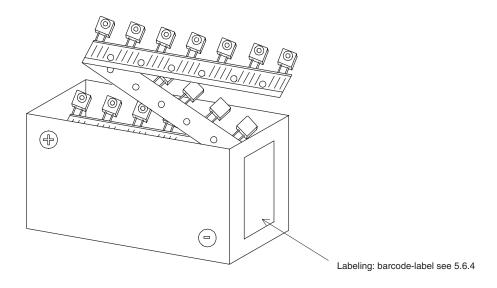
#### **PACKAGE DIMENSIONS** in millimeters

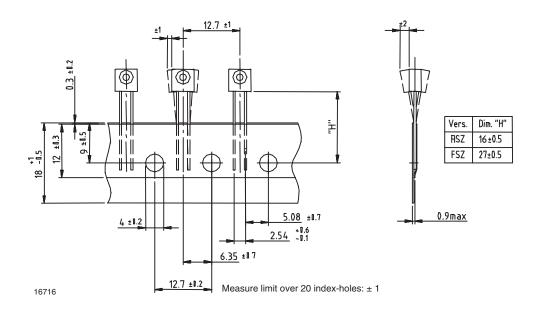




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#### TAPE AND AMMOPACK STANDARDS Dimensions in millimeters









Vishay

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