

7.62mm (0.3INCH) DUAL DIGIT NUMERIC DISPLAY

Part Number: DA03-11YWA Yellow

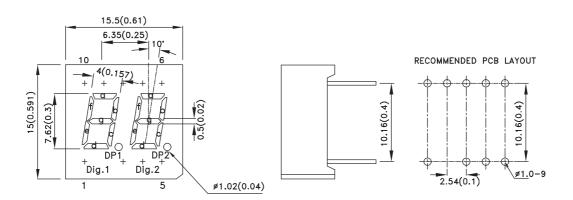
Features

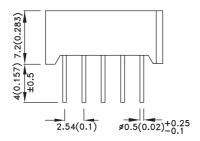
- 0.3 inch digit height.
- Low current operation.
- Excellent character appearance.
- Easy mounting on P.C. boards or sockets.
- Two digit package simplifies alignments & assembly.
- Mechanically rugged.
- Standard : gray face, white segment.
- RoHS compliant.

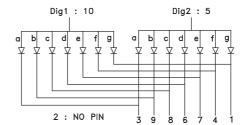
Description

The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode.

Package Dimensions& Internal Circuit Diagram











Notes

- 1. All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01")unless otherwise noted.
- 2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

 SPEC NO: DSAB8587
 REV NO: V.11A
 DATE: AUG/19/2014
 PAGE: 1 OF 6

 APPROVED: WYNEC
 CHECKED: Joe Lee
 DRAWN: Y.Liu
 ERP: 1302000054

Selection Guide

Part No.	Dice	Lens Type	lv (ucd) [1] @ 10mA		Description
			Min.	Тур.	
DA03-11YWA	Yellow (GaAsP/GaP)	White Diffused	1400	3100	Common Anode
			*560	*1100	

- 1. Luminous intensity/ luminous Flux: +/-15%.
 *Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Yellow	590		nm	IF=20mA
λD [1]	Dominant Wavelength	Yellow	588		nm	IF=20mA
Δλ1/2	Spectral Line Half-width	Yellow	35		nm	IF=20mA
С	Capacitance	Yellow	20		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Yellow	2.1	2.5	V	I==20mA
lR	Reverse Current	Yellow		10	uA	V _R =5V

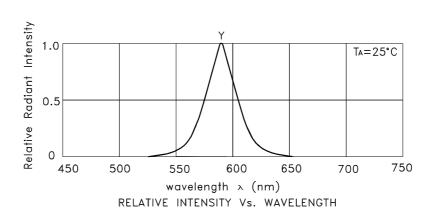
- 1.Wavelength: +/-1nm.
 2. Forward Voltage: +/-0.1V.
 3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

Absolute Maximum Ratings at TA=25°C

Parameter	Yellow	Units			
Power dissipation	75	mW			
DC Forward Current	30	mA			
Peak Forward Current [1]	140	mA			
Reverse Voltage	5	V			
Operating / Storage Temperature	-40°C To +85°C				
Lead Solder Temperature[2]	260°C For 3-5 Seconds				

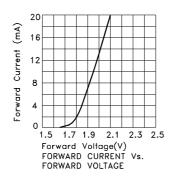
- Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. 2mm below package base.

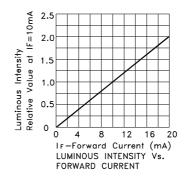
SPEC NO: DSAB8587 **REV NO: V.11A** DATE: AUG/19/2014 PAGE: 2 OF 6 APPROVED: WYNEC **CHECKED:** Joe Lee DRAWN: Y.Liu ERP: 1302000054

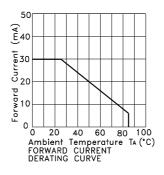


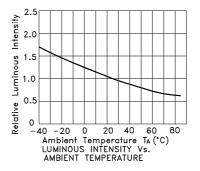
Yellow

DA03-11YWA





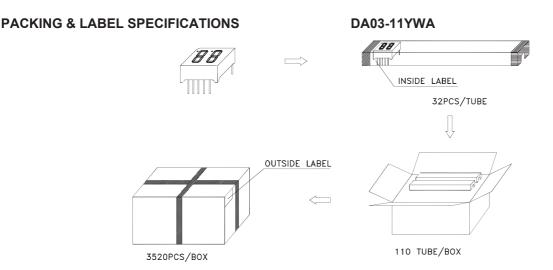


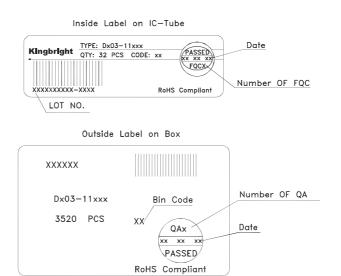


SPEC NO: DSAB8587 APPROVED: WYNEC REV NO: V.11A CHECKED: Joe Lee DATE: AUG/19/2014

DRAWN: Y.Liu

PAGE: 3 OF 6 ERP: 1302000054





Terms and conditions for the usage of this document

- 1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- 2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
- 3. When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
- 4. The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening liabilities, such as automotive or medical usage, please consult with Kingbright representative for further assistance.
- 5. The contents and information of this document may not be reproduced or re-transmitted without permission by Kingbright.
- 6.All design applications should refer to Kingbright application notes available at http://www.kingbright.com/application notes

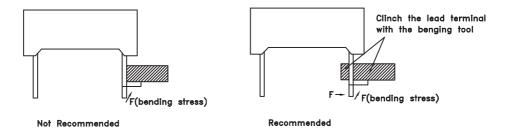
 SPEC NO: DSAB8587
 REV NO: V.11A
 DATE: AUG/19/2014
 PAGE: 4 OF 6

 APPROVED: WYNEC
 CHECKED: Joe Lee
 DRAWN: Y.Liu
 ERP: 1302000054

THROUGH HOLE DISPLAY MOUNTING METHOD

Lead Forming

Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

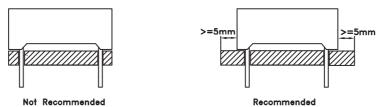


Installation

- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



3.The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.

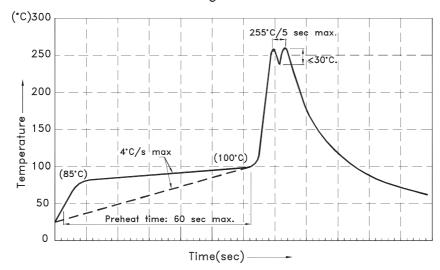


SPEC NO: DSAB8587 RE
APPROVED: WYNEC CHI

REV NO: V.11A CHECKED: Joe Lee DATE: AUG/19/2014 DRAWN: Y.Liu

PAGE: 5 OF 6 ERP: 1302000054

Recommended Wave Soldering Profiles:



Notes:

- 1.Recommend pre—heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
- 2.Peak wave soldering temperature between 245°C \sim 255°C for 3 sec (5 sec max).
- 3.Do not apply stress to the epoxy resin while the temperature is above 85°C.
- 4.Fixtures should not incur stress on the component when mounting and during soldering process. 5.SAC 305 solder alloy is recommended.
- 6.No more than one wave soldering pass.
- 7.During wave soldering, the PCB top-surface temperature should be kept below 105°C.

Soldering General Notes:

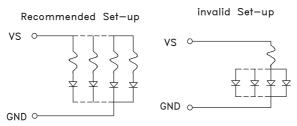
- 1. Through—hole displays are incompatible with reflow soldering.
- 2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

- 1.Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts .And the devices should not be washed for more than one minute.

CIRCUIT DESIGN NOTES

- 1.Protective current—limiting resistors may be necessary to operate the Displays.
- 2.LEDs mounted in parallel should each be placed in series with its own current—limiting resistor.



SPEC NO: DSAB8587 APPROVED: WYNEC REV NO: V.11A CHECKED: Joe Lee DATE: AUG/19/2014 DRAWN: Y.Liu PAGE: 6 OF 6 ERP: 1302000054