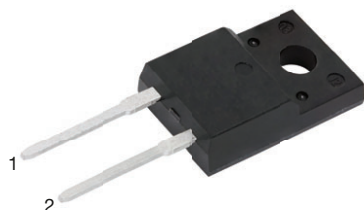
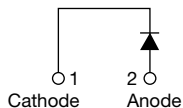


Hyperfast Rectifier, 8 A FRED Pt®



2L TO-220 FullPAK



FEATURES

- Hyperfast recovery time, extremely low Q_{rr}
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package ($V_{INS} = 2500 V_{RMS}$)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	8 A
V_R	600 V
V_F at I_F	1.5 V
t_{rr} (typ.)	14 ns
T_J max.	175 °C
Package	2L TO-220 FullPAK
Circuit configuration	Single

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current in DC	$I_{F(AV)}$	$T_C = 105\text{ °C}$	8	A
Non-repetitive peak surge current	I_{FSM}	$T_J = 25\text{ °C}$	80	
Operating junction and storage temperatures	T_J, T_{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	V_F	$I_F = 8\text{ A}$	-	2.5	3.4	
		$I_F = 8\text{ A}, T_J = 150\text{ °C}$	-	1.5	2.0	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	0.02	30	μA
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	21	150	
Junction capacitance	C_T	$V_R = 600\text{ V}$	-	6	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8	-	nH

**DYNAMIC RECOVERY CHARACTERISTICS** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	14	18	ns
		$I_F = 8\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	15	24	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	17	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	33	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.6	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	4.3	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	22	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	77	-	
Reverse recovery time	t_{rr}	$T_J = 125\text{ }^{\circ}\text{C}$ $I_F = 8\text{ A}$, $dI_F/dt = 600\text{ A}/\mu\text{s}$, $V_R = 390\text{ V}$	-	26	-	ns
Peak recovery current	I_{RRM}		-	11	-	A
Reverse recovery charge	Q_{rr}		-	150	-	nC

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction-to-case	R_{thJC}		-	4.6	5.5	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction-to-ambient	R_{thJA}	Typical socket mount	-	-	70	
Typical thermal resistance, case-to-heatsink	R_{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2	-	g
			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style 2L TO-220 FullPAK	ETX0806FP			

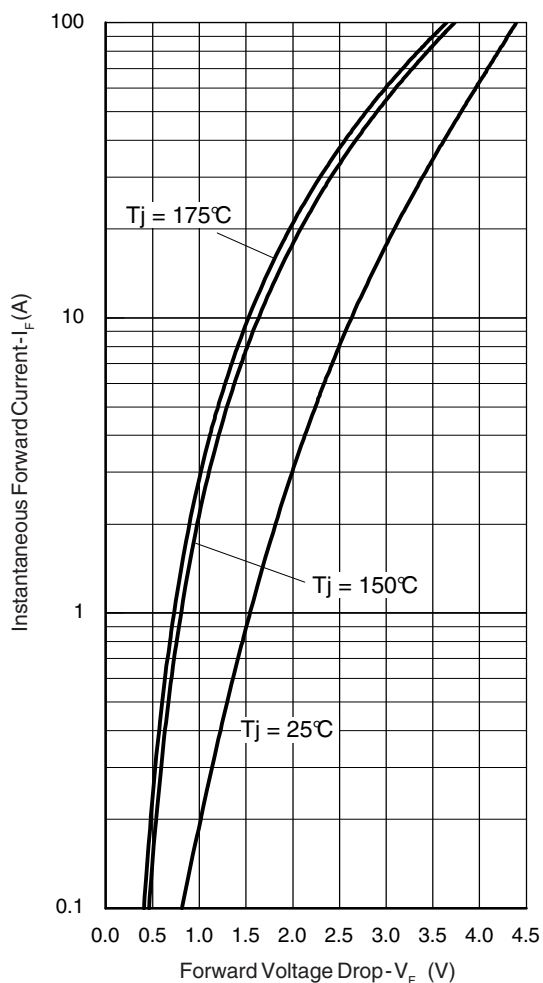


Fig. 1 - Typical Forward Voltage Drop Characteristics

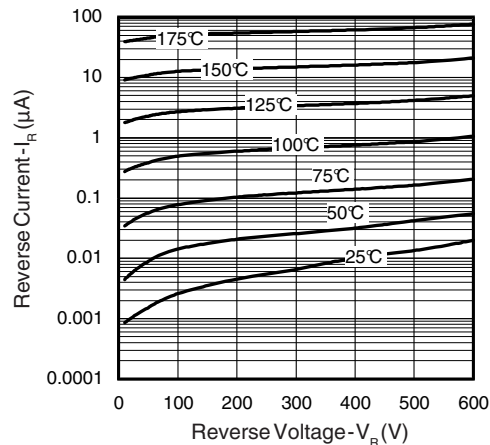


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

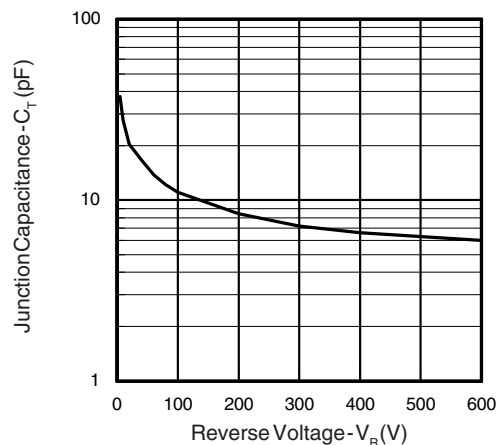
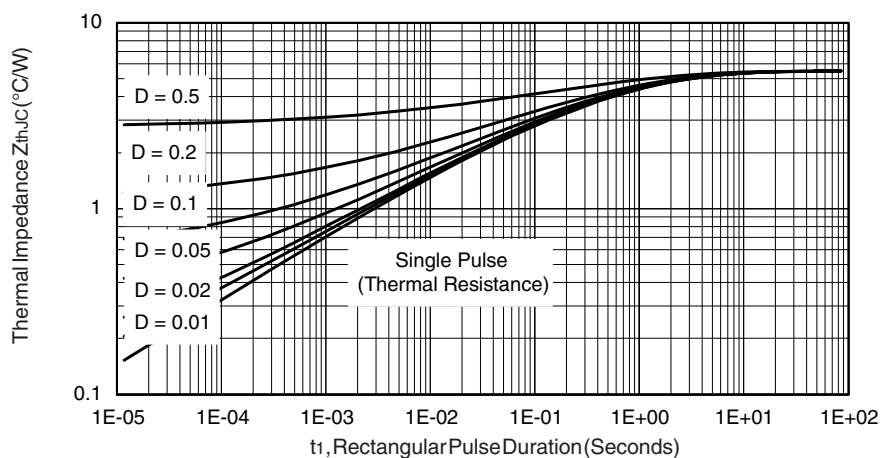


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

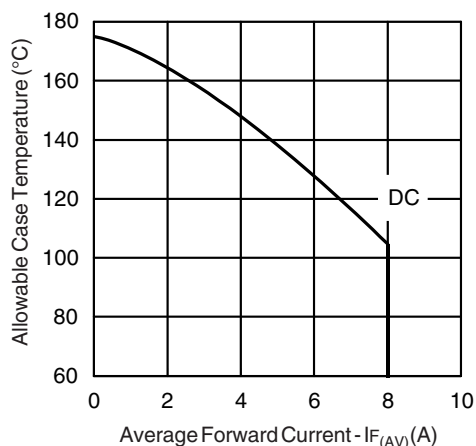


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

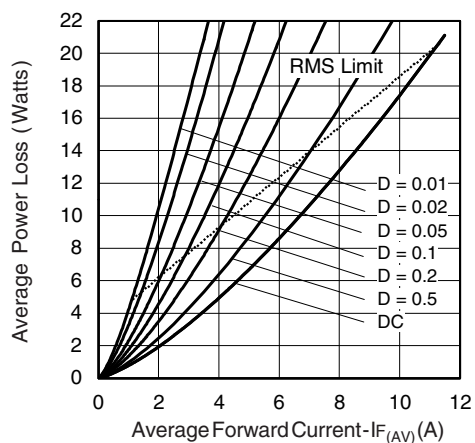


Fig. 6 - Forward Power Loss Characteristics

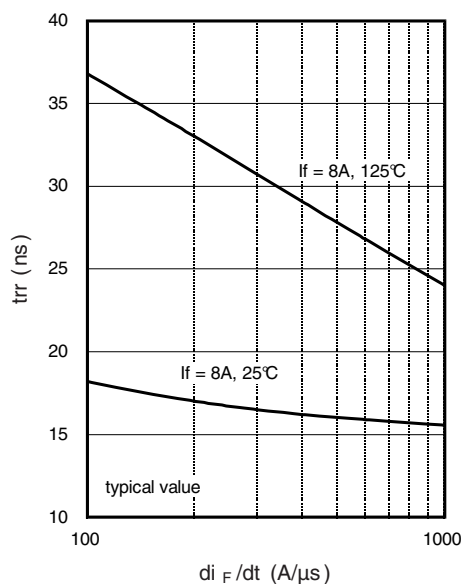


Fig. 7 - Typical Reverse Recovery vs. di_F/dt

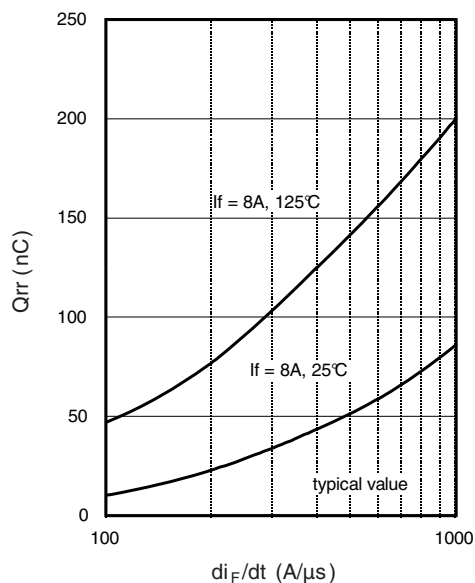
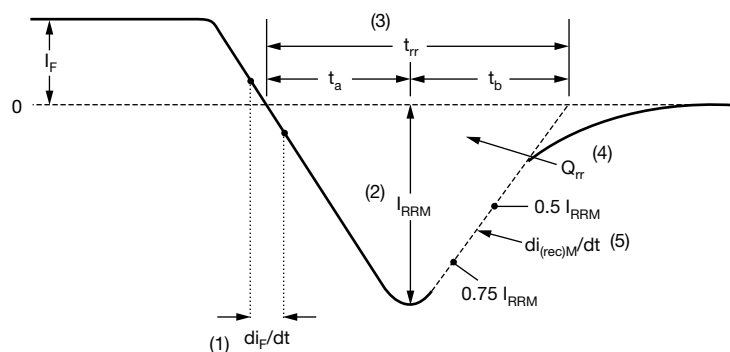


Fig. 8 - Typical Stored Charge vs. di_F/dt



- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}
- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	E	T	X	08	06	FP	-M3
	1	2	3	4	5	6	7	8

- 1** - Vishay Semiconductors product
- 2** - Circuit configuration:
E = single
- 3** - T = TO-220
- 4** - X = hyperfast recovery time
- 5** - Current code: 08 = 8 A
- 6** - Voltage code: 06 = 600 V
- 7** - FP = 2L TO-220 FullPAK
- 8** - Environmental digit:
-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-ETX0806FP-M3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96157
Part marking information	www.vishay.com/doc?95392



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